

Region Analysis for Imperative Languages

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Abstract

This paper presents a region inference framework designed specifically for imperative programs with dynamic allocation and destructive updates. Given an input program, the algorithm automatically translates it into an output program with region annotations on procedures and allocation commands, and with explicit region creation and removal commands.

Our framework formulates the analysis problem as a three-step algorithm. In the first phase, it infers region annotations for record declarations in the input language. Second, it performs a unification-based flow analysis of the program, inferring region types at each point in the program. In particular, it determines region types for allocation commands and procedure calls. In the third phase, it uses a single-pass algorithm to inspect each point in the program and insert region creation and removal commands in the control flow of the output program. This transformation ensures that regions are live whenever they are being used, while minimizing region lifetimes. The algorithm is simple, efficient, and provably correct.

Furthermore, we show that the framework can be extended with more aggressive analyses (at the expense of making it less modular or more complex), such as interprocedural region liveness or shape analysis, to further improve the accuracy and performance of memory management. More generally, our framework allows existing analysis technology for imperative languages, such as points-to or shape analysis, to be easily integrated and applied to the region inference problem.

1 Introduction

Memory management is a fundamental problem in languages, compilers, and virtually in any programming system. The difficulty of manually managing the data in the program has led to a large body of research to improve the data management process using either run-time systems, language support, or static analysis; all of these techniques have the common goal of making memory management simpler to use, safer, more efficient, and more precise.

Region allocation is an approach that has been proposed in the past decade for reaching these goals. In a region-based system, the compiler groups objects together in regions, and then deallocates all regions at once. Region-based systems have a number of appealing properties which makes them a good match for addressing the memory management problem: they efficiently deallocate larger groups of data; they are easier to use because they allow users to reason about collections of objects rather than individual objects; and they can be incorporated in the language and statically checked by the compiler to ensure safety. Two main research directions have emerged in this area: one concerns adding language support for regions, as in the case of the Cyclone [10], or RC [8] systems; the other direction is to develop static analyses for automatically inferring regions. The work presented in this paper falls in the latter category. So far, the proposed analysis techniques for region inference include the seminal work of Tofte and Talpin [15, 3], and

other similar, but more flexible systems. However, these approaches are all targeted to functional languages and their commonly used features, such as polymorphism and higher-order functions; they are not directly applicable to imperative languages such as Java or C.

This paper presents a region inference algorithm specifically formulated in the context of imperative languages. Although we use the same general ideas as the Tofte and Talpin approach, our algorithm focuses on control flow constructs, program state, dynamic allocation, and destructive updates. Given an input program, our algorithm automatically translates it into an output program with region-based data management. The output program has region-annotated procedures and allocation commands, and contains region creation and removal commands¹ at appropriate points in the control flow. The translation guarantees that the regions are live whenever they are being used; at the same time, the translation minimizes the lifetimes of each region in the program.

The algorithm consists of three steps. In the first phase, it computes region annotations for declarations of data structures in the program. Second, it infers region types for each variable, at each program point. In particular, it infers region types for each allocation command and procedure call. This step consists of two subphases: an intra-procedural flow analysis of each procedure; and an inter-procedural propagation of unifications. The insight behind having a flow analysis is that we want to allow variables to have different region types at different program points. For instance, a variable may point into different regions at different program points. Finally, in the third phase the algorithm uses a single-pass algorithm to infer the appropriate points in the program for inserting region creation and removal commands. Unlike many existing region-based systems [15, 10], our framework doesn't use lexically scoped region lifetimes at the intra-procedural level; a region can even be created or removed in our framework at multiple points, along different paths. However, regions are created and removed in the same procedure.

The paper also presents extensions to the basic algorithm which allow more aggressive optimizations and further improve the generated code. *Interprocedural creation and removal* performs an interprocedural region liveness analysis to determine when regions can be allocated late, in the invoked procedures, or removed early, before returning to the procedures that create them. The downside is that this adds more inter-procedural analysis overhead and makes the analysis less modular. The second extension, *object migration*, allows the compiler to extract objects from existing regions and move them to different regions, whenever the analysis can precisely determine that each extracted object is no longer connected to the old region. This transformation requires precise shape analysis information; it also requires a special command for the migration of objects across regions.

Our region inference framework has a number of appealing properties:

- *Simplicity*: The framework consists of two simple and lightweight algorithms.
- *Correctness*: The algorithm is provably correct.
- *Flexibility*: The basic algorithm can be optionally extended with more aggressive analyses.
- *Applicability*: The algorithm provides a core region analysis for imperative languages. With few changes, the algorithm can be extended and applied to widely used imperative languages such as Java, C, or C++.

The contributions of this paper are twofold. The first contribution is the formulation of the region analysis problem in the context of imperative languages. We give a clean, formal presentation of the proposed region analysis algorithms. The second contribution of the paper is

¹We use the terms *region creation* and *removal*, instead of *allocation* and *deallocation*, in order to avoid ambiguities between object allocation and region allocation.

that it provides a general region inference framework, where more aggressive analyses can build upon our basic algorithm and be applied to the region inference problem. In particular, it enables the direct application of existing analyses, such as points-to or shape analyses, to the memory management problem.

The paper is structured as follows. Sections 2 and 3 present the translation input and languages. Next, Section 4 shows an example of how our translation works. Section 5 presents the basic region inference algorithm, and Section 6 shows several extensions to this algorithm. Finally, we present related work in Section 7 and conclude in Section 8.

2 Input Language

Figure 1a) presents the abstract syntax for our input language. This is a simple imperative language which manipulates integers and dynamically allocated records, which we refer to as objects. A program consists of a sequence of type declarations for records, followed by a sequence of procedure declarations. For simplicity, we assume that procedures take exactly one argument and always return a value. The language allows directly and mutually recursive procedures. There is one special procedure `main` in the program, whose signature is `main : int \rightarrow int`. The execution of the program consists of running the body of this function with an argument value of zero.

The commands in the language include declarations of local variables, control flow commands (sequencing, conditionals, and loops), assignments, and return commands. Assignments, in turn, include updates to variables and to fields of heap objects; the value being assigned can be either the object returned by `new`; the result of a procedure call; or the result of an expression. Finally, expressions include variable values, field values, the null reference, integers constants, and arithmetic operations denoted by \oplus .

Appendix A.1 gives a precise specification using large-step operational semantics. The rules for evaluating expressions use judgments of the form $\langle S, H, \text{expr} \rangle \rightarrow v$, with the meaning that in the stack environment S and heap environment H , expression `expr` evaluates to value v . The rules for commands use judgments of the form $\langle S, H, \text{com} \rangle \rightarrow \langle S', H', v_{\text{ret}} \rangle$ meaning that in the stack environment S and heap environment H , the evaluation of command `com` yields a new stack S' and heap H' , and a return value v_{ret} . The latter is being used to ensure that the body of each procedure returns a value. We use a special value $v_{\text{ret}} = \text{nr}$ to indicate when a command doesn't return.

We informally describe the semantics of the input language as follows. An if statement executes its false branch if the condition expression evaluates to 0 (or null), and executes its true branch otherwise; similarly, the body of a while loop is executed as long as the condition expression is non zero (or not null). At declarations, variables are initialized with their default values (0 for integers, and null for references); the fields of dynamically allocated records are initialized with their default values; and variables must be declared before being used. Finally, the command representing the body of a procedure must always return a value.

The program types τ include integers, function types, references types to records, and a bottom type to model null references:

$$\tau ::= \text{int} \mid \tau_1 \rightarrow \tau_2 \mid \perp \mid \text{ref } (\bar{\tau} \bar{f})$$

The null reference can be assigned to a reference variable or field of any reference type.

Program	::=	typeDecl * procDecl*	Program	::=	typeDecl * procDecl*
typeDecl	::=	record s = (\bar{t} \bar{f})	typeDecl	::=	record s [r_0, \bar{r}] = (\bar{t} \bar{f})
procDecl	::=	t p (t z) = com	procDecl	::=	t p [\bar{r}] (t z) = com
t	::=	int s	t	::=	int s [r_0, \bar{r}]
com	::=	int x ; com s x ; com com ₁ ; com ₂ while (expr) com if (expr) then com ₁ else com ₂ x = expr x.f = expr x = new s x = p (y) return x	com	::=	... create r remove r x = new s in r x = p [\bar{r}] (y)
expr	::=	x x.f null n expr ₁ \oplus expr ₂			
x, y, z \in Variables f \in Fields s \in Records			where: r \in Region names		
p \in Procedures n \in Integer constants					

Figure 1: Syntax of the input and output languages

3 Output Language

Figure 1b) presents the abstract syntax for the output language. This language extends the input language in two ways. First, it adds regions annotations for declarations of record types and procedures. Second, it adds commands for manipulating regions: region creation and removal commands, dynamic allocation of objects into regions, and region based procedure calls.

For types, the construct $[r_0, \bar{r}]$ denotes the type of a reference which points into region r_0 , and is such that all of the regions reachable from this reference, except r_0 , are in the sequence \bar{r} . We call r_0 the *base region* of the record, and \bar{r} the *region parameters* of the record. This construct allows the compiler to grant access to the structure pointed to by such a reference only if it can guarantee that r_0 and all of the regions in \bar{r} have not been removed.

For procedure declarations, the construct $[\bar{r}]$ denotes the *formal region parameters* of the procedure. The sequence \bar{r} must include all of the regions in the argument and return type. For procedure calls, the construct \bar{r} denotes the *actual region parameters* at the call site. We require that all of the actual regions are live at the call site.

The commands for manipulating regions have the standard meanings: **create** r dynamically creates a new region, **remove** r dynamically removes a region, and **new** s in r dynamically allocates an object of record type s in region r . We require that region r is not created when **remove** is invoked, and that r is not removed when it is used in any other command or annotation.

Appendix A.2 provides a specification for the commands in the output language. The semantics is defined using judgments of the form $\langle S, H, R, \text{com} \rangle \rightarrow \langle S', H', R', v_{\text{ret}} \rangle$, where S, H, S', H' and v_{ret} are as before, and R and R' are the sets of live regions before and after the evaluation of command com . The majority of rules are identical to the corresponding rules from the input language, and are therefore not shown; we present only the rules which involve regions.

For types, we add region variables ρ and type schemes σ which include universal quantification over regions:

$$\begin{aligned}
\rho &\in \text{Region variables} \\
\sigma &::= \tau \mid \forall \bar{\rho} . \tau \\
\tau &::= \text{int} \mid \tau_1 \rightarrow \tau_2 \mid \perp \mid \text{ref } [\rho_0, \bar{\rho}] (\bar{\tau} \bar{f})
\end{aligned}$$

The recursive syntax for types corresponds to recursive types. For simplicity, we omit the

recursive type operator (μ) from the syntax, and express recursive types by their recursive type equations. For instance, the recursive type definition:

$$\tau = \text{ref } [\rho] \text{ (int data, } \tau \text{ next)}$$

stands for the recursive type:

$$\tau_r = \mu\tau . \text{ref } [\rho] \text{ (int data, } \tau \text{ next)}$$

Note that the universal quantification always encloses recursive types. In other words, we require that the recursive occurrences of types have identical region parameters. For instance, all the elements of a list or a tree structure have to be allocated in the same region. Consider the following type scheme for a list whose elements have a **data** field and a **next** link:

$$\sigma_{\text{List}} = \forall\rho . (\mu\tau . \text{ref } [\rho] \text{ (int data, } \tau \text{ next)})$$

This shows that all of the elements of the list must be allocated in the same region ρ .

4 Example

Figure 2 presents an example output program generated by our translation, that illustrates several features of our region inference algorithm. The region annotations are shown in bold. The input program is the same, but without the region annotations.

The program uses two functions, **main** and **copy**, to perform list manipulations. In this program, list elements and data are being allocated as different heap objects, as shown by the record declarations for **List** and **Data**; we refer to the list elements alone as the spine of the list. The **copy** procedure is a recursive procedure which takes a **List** as argument and produces a list with a different spine, but with the same elements as the original list. The **main** procedure works as follows. First, it creates a list using the first loop. At each iteration it allocates a new element in the spine, and a new data element. Next, the procedure performs a “re-spining” operation using the assignment $x = \text{copy}(x)$: it replaces its old spine with a new one; after the assignment, the old spine becomes garbage. Finally, the program runs another loop. At each iteration in this second loop, it creates a list y with a new spine, processes it, then discards the spine by nullifying y . The procedure **process** is omitted from the code; it takes a list as argument, uses it for internal computation, and returns an integer value.

We highlight several details of this transformation. First, the analysis identifies that procedure **copy** returns a list with data in the same region as the parameter list, as shown by the two occurrences of r_5 in the argument and the return value for **copy**. The transformation also annotates the allocation command in this function with region r_7 , which is the region for the spine of the returned list. The analysis infers that r_7 is a parameter region and does not generate a creation command for it in the body of the procedure; it is the responsibility of the caller to create this region.

Second, the analysis accurately determines the region creation points for the objects allocated in the two loops of procedure **main**. For the first loop, which iteratively creates a list, the analysis places the region creation commands for regions r_1 and r_2 outside the loop. For the second loop, the analysis determines that the lifetime of the region returned by **copy** doesn’t span outside the loop body; it therefore places the region creation and removal commands for this region inside the loop.

Third, for the “re-spining” assignment $x = \text{copy}(x)$, the transformation places a region creation command for region r_3 , the region for the new spine, before invoking **copy**. It also generates a

```

record Data [rd] = (int i)
record List [r1,rd] = (Data[rd] d, List [r1,rd] n)

int main(int i) {
    List x;

    create r1;
    create r2;
    while (i > 10) {
        Data d = new Data in r1;
        d.i = i;
        t = new List in r2;
        t.d = d;
        t.n = x; x = t;
        i = i + 1
    }

    create r3;
    x = copy[r1,r2,r3](x);
    remove r2;

    while (i) {
        create r4;
        List y = copy[r1,r3,r4](x);
        i = process[r1,r4](y);
        remove r4
    }
    remove r1;
    remove r3;
    return i
}

List[r5,r7] copy[r5,r6,r7]
(List[r5,r6] x) {
    List y,z,t;

    if (x) {
        y = new List in r7;
        y.d = x.d;
        z = x.n;
        t = copy[r5,r6,r7](z);
        y.n = t;
        return y
    } else {
        t = null;
        return t
    }
}

int process[r8,r9]
(List[r8,r9] x) {
    ...
}

```

Figure 2: Example translated program. The region annotations are shown in bold. The input program is the same, but without the region annotations.

region removal command for region r_2 , the region of the old spine, detecting that this region becomes garbage as a result of the assignment.

Fourth, the analysis automatically determines the appropriate actual region parameters at call sites. For the first call to `copy` it computes the region parameters $[r_1, r_2, r_3]$. For the second call, it computes parameters $[r_1, r_3, r_4]$.

Finally, this example shows that region lifetimes are not necessarily nested into each other. For instance, the placement of `create` r_3 before `remove` r_2 makes the lifetimes of these regions to overlap. Our the region inference algorithm doesn't require a particular discipline of region lifetimes, such as the stack discipline when regions are lexically scoped.

5 Region Inference Algorithm

This section presents the region analysis algorithm and the translation process. The algorithm consists of three phases. First, a *class declarations phase* infers region parameterized types for records. Second, a *record declaration phase* computes region types for variables at each point in the program. It consists of an intra-procedural flow analysis of each procedure; and an inter-procedural analysis which propagates type unifications between procedures. Third, a *translation phase* produces the output program. It generates region annotations for records, procedures, and allocations using the result from the first two phases. It also places region creation and removal statements at appropriate points in the control flow of the output program. We describe each of these steps in the following sections.

5.1 Record Declarations

The inference algorithm for record declarations is fairly straightforward. We require that the region parameters in each field of a record be a subset of the region parameters of the enclosing region; if the field is an instance of the recursive type, then these region parameters must be identical. For each record s , the analysis generates a fresh region name r_s to represent the base region of the record.

We define the translation using a function $\mathcal{T}[\cdot]$ which maps a piece of syntax from the input language into a piece of syntax in the output language. The translation of record declarations is:

$$\begin{aligned} \mathcal{T}[\text{record } s \ (\bar{t} \ \bar{f})] &= \text{record } \mathcal{T}[s] \ (\overline{\mathcal{T}[\bar{t}] \ \bar{f}}) \\ \mathcal{T}[\text{int}] &= \text{int} \\ \mathcal{T}[s] &= s \ [r_s, \bar{r}_{ps}] \quad (r_s \text{ fresh}) \end{aligned}$$

where each sequence \bar{r}_{ps} of region parameters is an arbitrary permutation of the set $RV(s)$ of region variables, computed as the least fixed point of the following constraints:

$$\frac{\text{record } s \ (\bar{t} \ \bar{f}) \quad s' \in \bar{t}}{RV(s') \subseteq RV(s)} \quad \overline{r_s \subseteq RV(s)}$$

We compute the least fixed point using a standard iterative computation which initializes each $RV(s)$ to the empty set, and then applies the rules until no changes occur.

For example, the translation of the following declaration:

```
record Data = (int x, int y)
record List = (Data data, List next)
record Iterator = (List crt)
```

is:

```

record Data [rd] = (int x, int y)
record List [rl, rd] = (Data [rd] data, List [rl, rd] next)
record Iterator [ri, rl, rd] = (List [rl, rd] crt)

```

At this point, the algorithm computes types for records, using the result of the above translation:

$$\frac{\text{record } s \ [r_0, \bar{r}] \ (\bar{t} \ \bar{f}) \quad \rho_0 = \text{fresh}(r_0) \quad \bar{\rho} = \text{fresh}(\bar{r})}{\text{type}(s) = \text{ref} \ [\rho_0, \bar{\rho}] \ (\bar{t}[\rho_0/r_0, \bar{\rho}/\bar{r}] \ \bar{f})}$$

The function `fresh` return a fresh region variable for each region name passed as argument.

5.2 Region Inference

The algorithm next infers region-polymorphic types for methods, and region types for variables at each program point. First, the algorithm uses a unification-based intra-procedural flow analysis which processes each method in the program once. Then, it performs an inter-procedural analysis which propagates type unifications beyond procedure boundaries. We describe each of these steps in turn.

The intra-procedural analysis of each procedure first builds a type environment Δ which assigns the most general types to procedures, call sites, and allocation commands, using fresh region variables. For procedures and call sites, the analysis assigns fresh region variables for each occurrence of a reference in the parameter and in the result value of the procedure being declared or invoked. For allocation sites, it assigns fresh region variables to the allocated record. To formalize the construction of this environment, we introduce labels for each allocation expression $a : \text{new } s$ and each call expression $c : p(y)$. The rules for computing Δ are as follows:

$$\frac{\begin{array}{c} t_2 \ p \ (t_1 \ z) \quad \tau_1 = \text{fresh}(\text{type}(t_1)) \\ \bar{\rho} = \text{FV}(\tau_1 \rightarrow \tau_2) \quad \tau_2 = \text{fresh}(\text{type}(t_2)) \end{array}}{\Delta \vdash p : \forall \bar{\rho} . \tau_1 \rightarrow \tau_2} \quad \frac{\begin{array}{c} \Delta \vdash p : \forall \bar{\rho} . \tau_1 \rightarrow \tau_2 \\ c : p(y) \quad \tau'_1 \rightarrow \tau'_2 = \text{fresh}(\tau_1 \rightarrow \tau_2) \end{array}}{\Delta \vdash c : \tau'_1 \rightarrow \tau'_2}$$

$$\frac{\begin{array}{c} a : \text{new } s \quad \text{type}(s) = \tau = \text{ref} \ [\rho_0, \bar{\rho}] \ (\bar{\tau} \ \bar{f}) \\ \rho'_0 = \text{fresh}(\rho_0) \quad \bar{\rho}' = \text{fresh}(\bar{\rho}) \end{array}}{\Delta \vdash a : \tau[\rho'_0/\rho_0, \bar{\rho}'/\bar{\rho}]}$$

where $\text{FV}(\tau)$ is the set of free region variables in τ . In the first two rules, the function `fresh` takes a type and replaces each of its region variables with fresh variables; it then returns the new type. In the last rule, `fresh` yields a fresh region variable for each of its argument regions. Note that we initialize all fields of a record with fresh region variables, although they are being initialized to `null` in the operational semantics. In other words we fix the regions for the values that will be later be stored in these fields. This is important for ensuring that the translation is sound.

A type environment Γ maps variables to their types. We define standard typing judgments for expressions $\Gamma \vdash e : \tau$, with the meaning that, in the environment Γ , expression e is well-typed and has type τ :

$$\frac{}{\Gamma \vdash n : \text{int}} \quad \frac{}{\Gamma \vdash \text{null} : \perp} \quad \frac{x : \tau \in \Gamma}{\Gamma \vdash x : \tau}$$

$$\frac{\Gamma \vdash \text{expr}_1 : \text{int} \quad \Gamma \vdash \text{expr}_2 : \text{int}}{\Gamma \vdash \text{expr}_1 \oplus \text{expr}_2 : \text{int}}$$

$$\frac{\Gamma \vdash x : \text{ref} \ [\rho_0, \bar{\rho}] \ (\bar{\tau} \ \bar{f}) \quad \tau f \in \bar{\tau} \bar{f}}{\Gamma \vdash x.f : \tau}$$

$$\begin{array}{c}
\frac{\Delta, \Gamma \cup \{x \mapsto \text{int}\} \vdash \text{com}, \Gamma' \cup \{x \mapsto \tau\}, E}{\Delta, \Gamma \vdash \text{int } x; \text{com}, \Gamma', E} \\
\\
\frac{\Delta, \Gamma \cup \{x \mapsto \perp\} \vdash \text{com}, \Gamma' \cup \{x \mapsto \tau\}, E}{\Delta, \Gamma \vdash s \ x; \text{com}, \Gamma', E} \\
\\
\frac{\Delta, \Gamma \vdash \text{com}_1, \Gamma_1, E_1 \quad \Delta, \Gamma_1 \vdash \text{com}_2, \Gamma_2, E_2}{\Delta, \Gamma \vdash \text{com}_1; \text{com}_2, \Gamma_2, E_1 \cup E_2} \\
\\
\frac{\Delta, \Gamma \vdash \text{com}_1, \Gamma_1, E_1 \quad \Delta, \Gamma \vdash \text{com}_2, \Gamma_2, E_2 \quad (E, \Gamma) = \text{unify}(\Gamma_1, \Gamma_2)}{\Delta, \Gamma \vdash \text{if } (\text{expr}) \text{ then } \text{com}_1 \text{ else } \text{com}_2, \Gamma, E_1 \cup E_2 \cup E} \\
\\
\frac{\Delta, \Gamma_1 \vdash \text{com}, \Gamma_2, E_1 \quad (E, \Gamma_1) = \text{unify}(\Gamma, \Gamma_2)}{\Delta, \Gamma \vdash \text{while } (\text{expr}) \text{ com}, \Gamma_1, E_1 \cup E}
\end{array}$$

Figure 3: Analysis rules for variable declarations and control flow constructs

Next, the algorithm performs the actual type inference via unification. It uses a flow analysis which generates unification constraints as a result of analyzing commands. Each unification constraint in E is a pair of unified region variables. The reason for using a flow analysis instead of a flow-insensitive approach is the presence of destructive updates in our input and output languages. Destructive updates to variables in the program yields new types for the updated variables, with new region instantiations. The algorithm must compute these types in a flow sensitive manner. The result of the algorithm is a type environment Γ at each program point.

We formulate our flow analysis for region inference using judgments of the form $\Delta, \Gamma \vdash \text{com}, \Gamma', E$, which express the fact that in the typing environment Δ for procedures and allocations, and given the region type environment Γ for variables, the command com yields a new type environment Γ' and a set of unification constraints E . Figure 3 shows the analysis rules for variable declarations and control flow constructs. For variable declarations, the initialize types of references to \perp and the types of integer variables to int .

The rules for control flow merge type information at control flow points using the unification function unify . This function takes two type environments, unifies the types of the corresponding variables, and yields the generated unification constraints and a resulting type environment. The reason for returning a type environment is that the analysis must handle subtyping relations for bottom types \perp) Unifying the bottom type \perp with another type τ , yields no unification constraints, and returns τ in the environment. If none of the arguments to unify are \perp , the unification function recurses on the structures of the types.

Note that the flow analysis domain is the cartesian product of lattices of height 1, corresponding to the two possible types for references, \perp and ref , ordered by subtyping. Hence, the analysis domain is a lattice with height equal to the number of reference variables. As a result, the number of iterations in the analysis of each loop is bounded by the number of reference variables updated in the loop body.

Figure 4 presents the analysis rules for assignments and return commands. In these rules, ret represents a dummy variable introduced in the type environment to keep track of the return type. The unification function works as before, but on pairs of types rather than type environments.

These represent the core rules of our analysis. They show how the analysis handles destructive

$$\begin{array}{c}
\frac{\Gamma \vdash \text{expr} : \tau}{\Delta, \Gamma \vdash x = \text{expr}, \Gamma[x \mapsto \tau], \emptyset} \\
\\
\frac{\Gamma \vdash x.f : \tau_1 \quad \Gamma \vdash \text{expr} : \tau_2 \quad (E, _) = \text{unify}(\tau_1, \tau_2)}{\Delta, \Gamma \vdash x.f = \text{expr}, \Gamma, E} \\
\\
\frac{\Delta \vdash a : \tau}{\Delta, \Gamma \vdash x = a : \text{new } s, \Gamma[x \mapsto \tau], \emptyset} \\
\\
\frac{\Delta \vdash c : \tau_1 \rightarrow \tau_2 \quad y : \tau_3 \in \Gamma \quad (E, _) = \text{unify}(\tau_1, \tau_3)}{\Delta, \Gamma \vdash x = c : p(y), \Gamma[x \mapsto \tau_2], E} \\
\\
\frac{x : \tau_1 \in \Gamma \quad \text{ret} : \tau_2 \in \Gamma \quad (E, _) = \text{unify}(\tau_1, \tau_2)}{\Delta, \Gamma \vdash \text{return } x, \emptyset, E}
\end{array}$$

Figure 4: Analysis rules for assignments and return.

updates of variables and heap objects. The main observation is that updates to stack locations also update their types, while updates to heap locations require type unification. We refer to the former as *strong type updates*, and to the latter as *weak type updates*. The reason for having two kinds of updates lays in the reachability properties of the updated locations. Remember that the region types of variables model all of the regions reachable from that variable; the type update of a variable must maintain this invariant. But updates in the program can change the reachability information in two ways: 1) they may change the regions reachable from the updated variable; and 2) they may change the reachability information for other locations that reach the updated location. Therefore, the analysis must correctly model these two effects. Our analysis performs strong type updates for variable assignments (rules for $x = \dots$), and performs type unification, i.e. weak type updates, for modifications of heap locations (rule for $x.f = \dots$); these rules correctly model the changes in reachability, according to the language semantics. Strong type updates are possible because our language forbids pointers to variables. Our rules are applicable to languages with similar semantics, like Java.

For pointer-based languages like C, the compiler can apply strong type updates only when it is able to determine that the assigned variable is not pointed to by other locations. Existing pointer analyses can give such guarantees and extend the above analysis rules in those cases. Similarly, shape analyses can provide guarantees that destructive heap updates only modify individual, disconnected heap locations; this would enable the application of strong type updates on heap locations as well.

The overall analysis of the current procedure p instantiates its argument and return type in the environment, then analyzes its body:

$$\frac{\begin{array}{c} t_2 \ p \ (t_1 \ z) = \text{com} \quad \Delta \vdash p : \forall \bar{\rho} . \tau_1 \rightarrow \tau_2 \\ \Delta, \{z \mapsto \tau_1, \text{ret} \mapsto \tau_2\} \vdash \text{com}, \Gamma, E_p \end{array}}{\vdash E_p}$$

The analysis result is the set E_p of unification constraints for procedure p . Its transitive closure yields the sets of unified region variables. Each region variable is then replaced with the representative of its equivalence class.

In the second part of the region inference phase, the algorithm performs inter-procedural region unifications, to ensure that the region type of each procedure at each call site is a valid

$$\begin{array}{c}
\Delta_P \vdash p : \forall \bar{\rho} . \tau_1 \rightarrow \tau_2 \quad x = c : p(y) \\
\rho_i, \rho_j \in \bar{\rho} = \{\rho_k\}_{1,n} \quad (\rho_i = \rho_j) \in E_{inter} \\
\Delta_P \vdash s : \tau'_1 \rightarrow \tau'_2 \quad \bar{\rho}' = FV(\tau'_1 \rightarrow \tau'_2) \\
\hline
\frac{(\rho = \rho') \in E_{intra}}{(\rho = \rho') \in E_{inter}} \quad \frac{}{(\rho'_i = \rho'_j) \in E_{inter}}
\end{array}$$

Figure 5: Inter-procedural analysis rules

instantiation of its declared region-polymorphic type. More precisely, if two region parameters of a procedure get unified, then the corresponding regions at each call site must also be unified. Such unifications can recursively propagate beyond multiple procedures. Figure 5 formalizes these unification rules. The environment Δ_P is the union of initialization environments of all procedures, and E_{intra} is the set of unifications produced by the intra-procedural analysis of all procedures. The final set of unifications E_{inter} is the least fixed point solution of these constraints.

5.2.1 Example

Figure 6 shows how the type inference algorithm works for the `copy` procedure from the example program in Section 4. The first column shows the code after the parameterized region types for `Data` and `List` have been inferred. For simplicity, we omit the region parameters in the recursive occurrence of `List` in its declaration. The second column shows the initializations for the type environment Δ . The algorithm assigns types to procedure `copy` (universally quantified over regions), to the allocation site `new s`, and to the recursive call `copy(z)`. For simplicity, we denote list types as $\text{List}[\rho, \rho']$ to mean the recursive type $\tau = \text{ref}[\rho, \rho'] (\text{ref}[\rho'](\text{int } i) \text{ d}, \tau \text{ n})$.

Program Code	Initialization Types	Computed Types	Unifications (intra)	Unifications (inter)
<pre> record Data[rd] = (int i) record List[rl, rd] = (Data[rd] d, List[rl, rd] n) List copy(List x) { List y, z, t; if (x) { y = new s; y.d = x.d; z = x.n; t = copy(z); y.n = t; return y } else { t = null; return t } } </pre>	<pre> copy : $\forall \rho_1, \rho_2, \rho_3, \rho_4 .$ $\text{List}[\rho_1, \rho_2] \rightarrow \text{List}[\rho_3, \rho_4]$ $\text{List}[\rho_5, \rho_6]$ $\text{List}[\rho_7, \rho_8] \rightarrow \text{List}[\rho_9, \rho_{10}]$ </pre>	<pre> x : $\text{List}[\rho_1, \rho_2]$ ret : $\text{List}[\rho_3, \rho_4]$ y, z, t : \perp y : $\text{List}[\rho_5, \rho_6]$ z : $\text{List}[\rho_1, \rho_2]$ t : $\text{List}[\rho_9, \rho_{10}]$ </pre>	<pre> $\rho_2 = \rho_6$ $\rho_7 = \rho_1, \rho_8 = \rho_2$ $\rho_5 = \rho_9, \rho_6 = \rho_{10}$ $\rho_3 = \rho_5, \rho_4 = \rho_6$ </pre>	<pre> $\rho_8 = \rho_{10}$ </pre>

Figure 6: Region type inference example. Inferred type of `copy` is $\forall \rho_1, \rho_2, \rho_3 . \text{List}[\rho_1, \rho_2] \rightarrow \text{List}[\rho_3, \rho_2]$, the type of the recursive call is $\text{List}[\rho_1, \rho_2] \rightarrow \text{List}[\rho_3, \rho_2]$, and the type of the allocation is $\text{List}[\rho_3, \rho_2]$

The third column shows the types that the analysis computes at each program point, and the last two columns shows the generated intra-procedural and inter-procedural unification constraints. In this example, the destructive update $y.n = t$ requires the unification of the types of the objects referenced by y and t , as a result of adding an edge between them. The next command (`return y`) then unifies the return type of the procedure with the type of t at this point. As a result of these two unification, the analysis infers that $\rho_2 = \rho_4$, i.e., that the returned list has the data in the same region as the argument list. Because these are region parameters in the signature of the function, the inter-procedural part of the analysis must propagate this constraint to the recursive call site. The generated inter-procedural constraint is $\rho_8 = \rho_{10}$. However, this relation was already implied by the intra-procedural constraints, so it doesn't change the existing region equivalence classes.

5.3 Translation

Once unification has been performed and types for procedures and allocation sites have been computed, it is straightforward to translate procedures and commands in the output language. The algorithm first maps each region variable to a region name: $\mathcal{T}[\rho] = r$ (where unified variables map to the same region name). It then uses this map to translate occurrences of record names in the program. If a record s is such that $\text{type}(s) = \text{ref}[\rho_0, \bar{\rho}] (\bar{\tau}\bar{f})$ then the algorithm translates each occurrence of s in the argument or result type of a procedure as: $\mathcal{T}[s] = s[\mathcal{T}[\rho_0], \overline{\mathcal{T}[\bar{\rho}]}]$, and each occurrence of s in an allocation command as: $\mathcal{T}[\text{new } s] = \text{new } s \text{ in } \mathcal{T}[\rho_0]$. For each procedure whose type in the environment Δ is $\forall \bar{\rho}. \tau_1 \rightarrow \tau_2$, we translate the name in its declaration as: $\mathcal{T}[p] = p[\overline{\mathcal{T}[\bar{\rho}]}]$. Finally, we use a similar translation for procedure names at each call sites, but using the actual region parameters $\bar{\rho}$ at these points, as given by region types of these calls in the environment Δ . All of the other constructs remain unchanged in the generated code.

At this point, the analysis has inferred region types for procedure definitions, allocation commands, and procedure calls. To complete the algorithm, the compiler needs to insert region creation and removal statements at appropriate points in the program. This is the main job of the translation phase.

Let us denote by R_p the region parameters of the currently analyzed procedure, by R_a the regions at allocation sites, and by R_c the regions at call sites. Of all these regions, only the ones in R_p are already created at the entry point of the procedure. For each region in $(R_a \cup R_c) - R_p$, the compiler must insert appropriate region creation commands to ensure that the required regions exist when the command is executed. We say that the regions in $(R_a \cup R_c) - R_p$ are *local* to the current procedure. We give a precise definition of the placement problem for region creation and removal commands, and then present our solution.

5.3.1 Problem definition

We introduce the notions of a definition, use, and liveness of a region, as follows. The *definition* of a region is the procedure entry point, for parameter regions, or the creation point, for local regions. The *uses* of a region are the program points where its corresponding region variable occurs in the type environment at that point. We say that a local region is *live* at a point if it is used at that point; parameter regions are live throughout the body of the procedure, regardless of whether they are being used or not. The reason is that the compiler doesn't know whether the caller holds live data in those regions, and it must conservatively assume they do.

With these definitions, the solution to the region placement problem must satisfy the following constraints:

- **Safety:** For each procedure and each path in the flow graph of that procedure, the following conditions must hold:

- Each use of a region must be preceded by a definition of the region, and there should be no removal of the region since the last creation of the region.
 - For each region there cannot be consecutive occurrences of creation or removal commands.
 - Each removal of a region must be preceded by a definition of that region.
- **Precision:** The placement of region creation and removal commands should minimize the lifetimes of each region.

We next argue that, given the above conditions and assuming no interprocedural analysis, the algorithm should not insert creation and removal commands for region parameters. First, the region parameters may contain live data in the caller, which means that, in the absence of interprocedural information, the compiler cannot remove them in the callee. As a result, it cannot insert creation sites either, because they are already defined at procedure entry and additional creation sites would violate the second safety condition above.

Hence, the problem of placement of region commands applies only to the local regions in $(R_a \cup R_c) - R_p$. For each of them, the algorithm must insert creation and removal commands that satisfy the safety and optimality conditions listed above.

5.3.2 Placement of Region Commands

Despite its apparent complexity, the command placement problem has a short solution: the compiler places a `create r` command at each point in the program where `r` becomes live, and inserts a `remove r` command at each point where it ceases to be live. There are two situations when a region becomes live:

1. When it is not live before a join point in the control flow, but is live after the join. The join points in the control flow occur at the end of conditional commands and at the entry points of while loops.
2. When the region is not live before an allocation site or a call site which uses that region.

There are three situations when a region ceases to be live:

1. When the region is live before an assignment, but not after, because the assignment updates the last variable whose type contains the region.
2. When a variable goes out of scope, and that variable is the last variable whose type contains the region. Variables go out of scope either at the end of structured scope declarations (`int x; c` or `s x; c`), or before return commands (which represent unstructured forms of scope termination points).

Intuitively, our approach for region command placement models a finite state machine which transitions between two states: one where the region is live, and another where it isn't live. At the beginning of the procedure, the state machine starts in the latter state. Then, during the execution of a program, it transitions between the two states whenever it reaches a point where the region becomes live or ceases to be live. Region creation and removal commands occur only on the transitions between these states.

With this automaton model, it is straightforward to check that the resulting program satisfies all three safety conditions. It is also easy to verify that the algorithm optimally places creation and removal commands, minimizing the region lifetimes under the given safety constraints. Moving a

$$\begin{array}{c}
\frac{\text{com} \equiv \text{if } (e) \text{ then } \text{com}_1 \text{ else } \text{com}_2 \quad r \notin \text{Live}(\text{com}_1 \bullet) \quad r \in \text{Live}(\text{com} \bullet)}{\text{com}_1 \bullet \implies \text{com}_1} \qquad \frac{\text{com} \equiv \text{if } (e) \text{ then } \text{com}_1 \text{ else } \text{com}_2 \quad r \notin \text{Live}(\text{com}_2 \bullet) \quad r \in \text{Live}(\text{com} \bullet)}{\text{com}_2 \bullet \implies \text{com}_2} \\
\\
\frac{\text{com} \equiv \text{while } (e) \text{ com}_1 \quad r \notin \text{Live}(\bullet \text{com}) \quad r \in \text{Live}(\bullet \text{com}_1)}{\bullet \text{com} \implies \text{create } r} \qquad \frac{\text{com} \equiv \text{while } (e) \text{ com}_1 \quad r \notin \text{Live}(\text{com}_1 \bullet) \quad r \in \text{Live}(\bullet \text{com}_1)}{\text{com}_1 \bullet \implies \text{create } r} \\
\\
\frac{\text{com} \equiv x = \text{new } s \text{ in } r \quad r \notin \text{Live}(\bullet \text{com})}{\bullet \text{com} \implies \text{create } r} \quad \frac{\text{com} \equiv x = p[\bar{r}](y) \quad r_i \in \bar{r} \quad r_i \notin \text{Live}(\bullet \text{com})}{\bullet \text{com} \implies \text{create } r_i}
\end{array}$$

Figure 7: Regions for Region Creation

$$\begin{array}{c}
\frac{\text{com} \equiv t \ x; \text{com}' \quad r \in \text{Live}(\text{com}' \bullet) \quad r \notin \text{Live}(\text{com} \bullet)}{\text{com} \bullet \implies \text{remove } r} \\
\\
\frac{r \in \text{Live}(\bullet \text{com}) \quad r \notin \text{Live}(\text{com} \bullet)}{\text{com} \bullet \implies \text{remove } r} \quad \frac{\text{com} \equiv \text{return } x; \quad r \in \text{Live}(\bullet \text{com})}{\bullet \text{com} \implies \text{remove } r}
\end{array}$$

Figure 8: Rules for Region Removal

region creation later or a region removal earlier immediately causes the program to use a region at a point where it hasn't been created yet, or has already been removed.

Figure 8 formalizes the region placement algorithm using relations of the form $\text{pp} \implies \text{create } r$ and $\text{pp} \implies \text{remove } r$, indicating that the algorithm inserts a region creation or removal command at program point pp . Given a command com , we denote by $\bullet \text{com}$ the program point before the command, and by $\text{com} \bullet$ the program point after the command. With the exception of the rule for local declarations (first removal rule), all of the commands com represent assignments. All the rules refer only to local regions $r \notin R_p$, since these are the only ones for which we insert such commands. For a program point pp , we denote by $\text{Live}(\text{pp})$ the set of live regions at this point, which includes all the regions that occur in the computed type environment at this program point.

5.4 Soundness

We briefly summarize the formal soundness properties of our algorithm. The safety condition is based on consistency relations between stacks, heaps, regions, values, and type information. Informally, a stack and heap in the output program is consistent with a stack and heap in the input program, written $(S_o, H_o) \sim (S_i, H_i)$, if there is a homomorphism between them. A value in the output program is consistent with a value in the input program, written $v_o \sim v_i$, if they are identical, except for the region information. A consistency relation $(S_o, H_o, R) \sim \Gamma(\text{pp})$ holds if the stack and heap structure (S_o, H_o) agrees with the type information $\Gamma(\text{pp})$ and if $R = \text{Live}(\Gamma(\text{pp}))$. Finally, a consistency relation $(v_o, H_o) \sim \Gamma(\text{pp})$ holds if the structure of the portion of heap H_o reachable from v_o is consistent with the type information. Given the type information Γ computed by our algorithm, the following two properties hold:

Progress. Assuming consistent starting states, if the input command doesn't get stuck, then neither does the transformed command.

$$\begin{aligned}
& c_o = \mathcal{T}[\llbracket c_i \rrbracket] \wedge \\
& \langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle \wedge \\
& (S_o, H_o) \sim (S_i, H_i) \wedge (S_o, H_o) \sim \Gamma(\bullet c_i) \\
\Rightarrow & \langle S_o, H_o, R, c_o \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle
\end{aligned}$$

Preservation. If the execution of the input and output commands starts with consistent states which agree with the type information, then states after the execution of the commands are also consistent, and agree with the type information :

$$\begin{aligned}
& c_o = \mathcal{T}[\llbracket c_i \rrbracket] \wedge \\
& \langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle \wedge \\
& \langle S_o, H_o, R, c_o \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle \wedge \\
& (S_o, H_o) \sim (S_i, H_i) \wedge (S_o, H_o) \sim \Gamma(\bullet c_i) \\
\Rightarrow & \begin{aligned}
& (S'_o, H'_o) \sim (S'_i, H'_i) \wedge v_o \sim v_i \\
& (S'_o, H'_o, R') \sim \Gamma(c_i \bullet) \text{ if } v_i = \text{nr} \\
& (v_o, H'_o) \sim \Gamma(\bullet c_i) \text{ if } v_i \neq \text{nr}
\end{aligned}
\end{aligned}$$

Appendix B provides a detailed definition of the consistency relations and gives full formal proofs for these results.

6 Extensions

This section presents extensions that improve the accuracy of the basic region inference algorithm.

6.1 Interprocedural Creation and Removal

Even though region lifetimes in our algorithm are not lexically scoped and don't follow a stack discipline, the region creation and removal commands are always placed in the same procedure. The reason for this is to keep the basic algorithm modular.

A simple extension is to relax this condition and perform interprocedural placement of region creation and removal commands. The downside is the loss of modularity in the algorithm. We can extend the algorithm with interprocedural computation of region liveness analysis as follows. A backwards dataflow analysis can determine when region parameters are always dead at the end of the procedure. It can then use this information to perform region removal in the invoked procedure. Another analysis can determine when region parameters are never used before the function is invoked. In that case, they can be treated as not defined at procedure entry.

The overall algorithm executes as follows. It first performs the flow analysis the same way as before. It then applies the interprocedural region liveness algorithms. Once region liveness is available at procedure entry and exit points, it incorporates it in the `Live` sets of our analyses and performs the placement of region commands according to the rules from Section 5.3.2.

6.2 Object migration

Another extension to the region inference algorithm is to support migration of objects between regions. We want to allow individual objects to move from a region r_1 to a region r_2 , if we are guaranteed that the object was completely disconnected from the region r_1 before moving it to the region r_2 . To support this feature we extend the output language with an additional command:

$$\text{com} ::= \dots \mid \text{move } x \ r_1 \ r_2$$

This extension requires a precise analysis information about when individual object become completely disconnected from the rest of the heap. This kind of information can be provided, for instance, by existing pointer and shape analysis algorithms. We just assume an interface of such an analysis, which provides a function $D : PP \rightarrow \mathcal{P}(V)$, that, given program point $pp \in PP$, returns the set of variables that point to individual objects, unaliased and disconnected in the heap. Here V is the set of program variables.

The object migration analysis requires only two simple extensions to the basic algorithm. The first change is that the analysis inserts the `move` instructions in the program before running the region inference algorithm. It determines the program points where to insert these instructions using the available shape or points-to information in D . Object migration situations can only arise for statements of the form $x.f = \text{expr}$, which may disconnect the object from one region and then connect it to another. When expr is `null`, the assignment only disconnects the object. Since the analysis must identify the point when objects become disconnected, we assume that the program is in a form such that each assignment $x.f = \text{expr}$ is preceded by a nullification assignment $x.f = \text{null}$. The analysis uses the rewriting rule below to place migration commands when objects become disconnected:

$$\frac{\text{com} \equiv x.f = \text{null} \quad x \notin D(\bullet \text{com}) \quad x \in D(\text{com} \bullet) \quad \rho_0, \rho_1 \text{ fresh}}{\text{com} \Rightarrow \text{com} ; \text{move } x \rho_0 \rho_1}$$

The analysis then runs the basic region inference algorithm on the resulting program, using the analysis rules from Section 5.2. For analyzing move commands, it uses the following rule:

$$\frac{\Gamma \vdash x : \text{ref } [\rho', \bar{\rho}] (\bar{\tau}f) \quad \tau' = \text{ref } [\rho_1, \bar{\rho}] (\bar{\tau}f)}{\Delta, \Gamma \vdash \text{move } x \rho_0 \rho_1, \Gamma[x \mapsto \tau'], \{\rho' = \rho_0\}}$$

The object migration extension requires no other modification of the algorithm.

Figure 9 shows an example of object migration. The program creates two lists `a` and `b`, then moves an element from list `a` to list `b`. One can think of this example as modeling a process scheduler that manages processes using lists; this operation can be thought as moving a process from the running queue to the blocked queue of the scheduler.

The first column shows the original code before performing any transformation, the second column shows the result using the original region inference algorithm, and the third shows the resulting code after the migration extension. This example shows that the original algorithm cannot support object migration, and requires the two lists to be allocated in the same region. This is a result of not being able to perform strong type updates during the flow analysis of field assignments. The object migration extension essentially uses precise shape analysis in the premises of the analysis rules to enable strong type updates even for heap locations, whenever can determine that it is safe to do so.

7 Related Work

The algorithm presented in this paper is closely related to the region inference algorithm of Tofte and Talpin [15, 3]. They present a region inference algorithm for a simply typed lambda calculus, which translates expressions in the input program into region-annotated terms in the output program. The regions in their output language are lexically scoped, which imposes a stack discipline on the lifetimes of regions. In an extended version of their algorithm, they propose additional constructs such as region resetting, which alleviate the shortcomings due to the stack discipline constraints on region lifetimes. Further extensions of their framework for ML references include analysis rules similar to our typing rules for destructive updates in our algorithm.

Original Code	Region inference	Migration extension
<pre> record List = (int i, List n) int main(int x) { List a,b,t; a = makeList(x); b = makeList(x); t = a; a = t.n; t.n = null; t.n = b; b = t; return x }</pre>	<pre> record List[r1] = (int i, List[r1] n) int main(int x) { List a,b,t; create r1; a = makeList [r1] (x); b = makeList [r1] (x); t = a; a = t.n; t.n = null; // at this point the algorithm // unifies the regions of a and b t.n = b; b = t; remove r1; return x }</pre>	<pre> record List[r1] = (int i, List[r1] n) int main(int x) { List a,b,t; create r1; a = makeList [r1] (x); create r2; b = makeList [r2] (x); t = a; a = t.n; t.n = null; move t r1 r2; t.n = b; b = t; remove r1; remove r2; return x }</pre>

Figure 9: Example of extending the region inference algorithm with object migration.

Aiken, Fahndrich, and Levien [1] build on the Tofte/Talpin algorithm and extend it with separate region allocation and deallocation constructs. Object lifetimes are unrestricted and need not follow a stack discipline. The placement of region allocation and deallocation constructs is determined by building and solving a system of constraints which impose that regions are allocated when they are used. The separation of allocation and deallocation points yields significant improvements in performance compared to the Tofte/Talpin algorithm. Henglein, Makhholm, and Niss [11] present a region inference system whose output language extends the core lambda calculus with imperative commands for region manipulation. They also decouple region allocation and deallocation. Moreover, they propose commands for region aliases and region assignments.

Motivated by the success of the region-inference work, researchers have recently investigated the alternative of providing language and compiler support for regions. In contrast to the research on region inference, this work has focused more on imperative languages. The RC system [7, 8] provides run-time support for C programs, using library functions for allocation and deallocation of regions, and allocation of objects in specific regions. The system uses reference counts to identify unsafe region deallocations. The Cyclone language [10], a memory-safe version of the C language, uses a more sophisticated region system, providing a full featured type system and run-time support for regions. The system statically checks region annotations and statically ensures safe region deallocation. We believe that our region inference algorithm is a good match for automatically providing region annotations in such systems.

Another related area of research is that of statically inferring object lifetimes. Early work in this direction [13] proposed intraprocedural and interprocedural dataflow analyses. More recent work is based on escape analysis analysis to determine object lifetimes and enable stack allocation for objects whenever objects do not escape from the current procedure [4, 6, 16, 5, 9]. Although they have the similar goals ours, these algorithms are significantly more complex than the unification-based allocation algorithm in this paper. Moreover, the region polymorphic types

of procedures allow our algorithm to characterize object lifetimes even when they escape from the procedure that created them, which the escape-based algorithms don't. Lattner and Adve [12] propose an algorithm for pool allocation of objects. Their approach is to inline procedures to duplicate allocation sites into their callers. Duplicated allocation sites will then allocate objects in different regions. This approach, however, doesn't work well in the presence of recursion or for programs with deep call graphs, because of the exponential blow-up in code size (or analysis abstractions). Our region approach is cleaner and more general.

Finally, precise shape analysis algorithms such as [14] can complement our algorithm to improve the analysis results. These algorithms can provide accurate information about when individual objects are being removed from their regions. The results of a combined region inference and shape analysis can then be used in conjunction with run-time systems such as the recently proposed *reaps* [2], which combine region allocation with individual deallocation of objects within each region.

8 Conclusions

In this paper we have presented a region inference framework specifically designed for imperative programs with control flow constructs, dynamic allocation, and destructive updates of variables and heap locations. The basic algorithm in this framework first infers region types for variables, procedures, and dynamic allocation commands using a unification based flow analysis. Then, the analysis uses a one-pass algorithm to place region creation and removal commands in the program. We show that the basic framework can be augmented with more aggressive analyses, such as inter-procedural region liveness analysis, live variable analysis, and shape analysis, to achieve better memory management. Because of its simplicity and flexibility, we believe that this framework is a first step towards the adoption of region inference into existing popular imperative programming languages such as Java or C.

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A Semantics

The following sections present the large-step operational semantics for the input and output languages.

We use the following notation for maps. If M is a map, then $M \cup \{x \mapsto v\}$ represents a map M' which *extends* M with a value for x : $x \notin \text{dom}(M)$, $M'(x) = v$ and $M'(y) = M(y)$, for $y \neq x$. Hence, $\text{dom}(M') = \text{dom}(M) \cup \{x\}$. Furthermore, $M \cup \{x \mapsto v\}$ represents a map M'' which *updates* M with a new value for x : $x \in \text{dom}(M)$, $M''(x) = v$ and $M''(y) = M(y)$, for $y \neq x$. Hence, $\text{dom}(M'') = \text{dom}(M)$.

A.1 Semantics of Input Language

The domains and domain equations for the operational semantics of the input language are:

$$\begin{aligned} \text{Value} &= \text{Integer} + \{\text{null}\} + \text{Location} \\ \text{Retval} &= \text{Value} + \{\text{nr}\} \\ \text{Stack} &= \text{Variable} \rightarrow \text{Value} \\ \text{Heap} &= \text{Location} \rightarrow (\text{Field} \rightarrow \text{Value}) \end{aligned}$$

The value `nr` indicates that a command doesn't return.

A.1.1 Expressions

The semantics of expressions in the input language is defined using a relation $\langle S, H, \text{expr} \rangle \rightarrow v$ in the domain:

$$(\text{Stack} \times \text{Heap} \times \text{Expr}) \rightarrow \text{Value}$$

The meaning of $\langle S, H, \text{expr} \rangle \rightarrow v$ is that, given a stack S and a heap H , expression `expr` evaluates to value v . We define this evaluation relation using the following rules:

$$\begin{array}{c} \frac{S(x) = v}{\langle S, H, x \rangle \rightarrow v} \quad \frac{}{\langle S, H, n \rangle \rightarrow n} \quad \frac{}{\langle S, H, \text{null} \rangle \rightarrow \text{null}} \\[10pt] \frac{\langle S, H, e_1 \rangle \rightarrow n_1 \quad \langle S, H, e_2 \rangle \rightarrow n_2 \quad n = n_1 \oplus n_2}{\langle S, H, e_1 \oplus e_2 \rangle \rightarrow n} \quad \frac{S(x) = l_1 \quad H(l_1)(f) = v}{\langle S, H, x.f \rangle \rightarrow v} \end{array}$$

A.1.2 Commands

We express the semantics of commands using a relation $\langle S, H, \text{com} \rangle \rightarrow \langle S', H', v_{\text{ret}} \rangle$ in the domain:

$$(\text{Stack} \times \text{Heap} \times \text{Com}) \rightarrow (\text{Stack} \times \text{Heap} \times \text{Retval})$$

The meaning of the relation $\langle S, H, \text{com} \rangle \rightarrow \langle S', H', v \rangle$ is that, given a stack S and a heap H , the evaluation of command `com` yields a new stack S' , a new heap H' and a result value v . The evaluation rules are:

$$\begin{array}{c} \frac{\langle S, H, e \rangle \rightarrow v}{\langle S, H, x = e \rangle \rightarrow \langle S[x \mapsto v], H, \text{nr} \rangle} \\[10pt] \frac{S(x) = l \quad \langle S, H, e \rangle \rightarrow v}{\langle S, H, x.f = e \rangle \rightarrow \langle S, H[l \mapsto H(l)[f \mapsto v]], \text{nr} \rangle} \end{array}$$

$$\frac{s : \text{ref}(\bar{t} \ \bar{f}) \quad m = \text{init}(\bar{t} \ \bar{f})}{\langle S, H, x = \text{new } s \rangle \rightarrow \langle S[x \mapsto l], H \cup \{l \mapsto m\}, \text{nr} \rangle}$$

$$\frac{S(y) = v_1 \quad t \ p \ (t \ z) = c \quad \langle \{z \mapsto v_1\}, H, c \rangle \rightarrow \langle S_1, H_1, v \rangle \quad v \neq \text{nr}}{\langle S, H, x = p(y) \rangle \rightarrow \langle S[x \rightarrow v], H_1, \text{nr} \rangle}$$

$$\frac{S(x) = v}{\langle S, H, \text{return } x \rangle \rightarrow \langle \emptyset, H, v \rangle}$$

$$\frac{\langle S \cup \{x \mapsto 0\}, H, c \rangle \rightarrow \langle S' \cup \{x \mapsto v\}, H', v_r \rangle}{\langle S, H, \text{int } x; c \rangle \rightarrow \langle S', H', v_r \rangle}$$

$$\frac{\langle S \cup \{x \mapsto \text{null}\}, H, c \rangle \rightarrow \langle S' \cup \{x \mapsto v\}, H', v_r \rangle}{\langle S, H, s \ x; c \rangle \rightarrow \langle S', H', v_r \rangle}$$

$$\frac{\langle S, H, c_1 \rangle \rightarrow \langle S_1, H_1, v \rangle \quad v \neq \text{nr}}{\langle S, H, c_1; c_2 \rangle \rightarrow \langle S_1, H_1, v \rangle}$$

$$\frac{\langle S, H, c_1 \rangle \rightarrow \langle S_1, H_1, \text{nr} \rangle \quad \langle S_1, H_1, c_2 \rangle \rightarrow \langle S_2, H_2, v_r \rangle}{\langle S, H, c_1; c_2 \rangle \rightarrow \langle S_2, H_2, v_r \rangle}$$

$$\frac{\langle S, H, e \rangle \rightarrow v \quad v \notin \{0, \text{null}\} \quad \langle S, H, c_1 \rangle \rightarrow \langle S_1, H_1, v_r \rangle}{\langle S, H, \text{if } (e) \text{ then } c_1 \text{ else } c_2 \rangle \rightarrow \langle S_1, H_1, v_r \rangle}$$

$$\frac{\langle S, H, e \rangle \rightarrow v \quad v \in \{0, \text{null}\} \quad \langle S, H, c_2 \rangle \rightarrow \langle S_2, H_2, v_r \rangle}{\langle S, H, \text{if } (e) \text{ then } c_1 \text{ else } c_2 \rangle \rightarrow \langle S_2, H_2, v_r \rangle}$$

$$\frac{\langle S, H, e \rangle \rightarrow v \quad v \in \{0, \text{null}\}}{\langle S, H, \text{while } (e) \ c \rangle \rightarrow \langle S, H, \text{nr} \rangle}$$

$$\frac{\langle S, H, e \rangle \rightarrow v \quad v \notin \{0, \text{null}\} \quad \langle S, H, c \rangle \rightarrow \langle S_1, H_1, v' \rangle \quad v' \neq \text{nr}}{\langle S, H, \text{while } (e) \ c \rangle \rightarrow \langle S_1, H_1, v' \rangle}$$

$$\frac{\langle S, H, e \rangle \rightarrow v \quad v \notin \{0, \text{null}\} \quad \langle S, H, c \rangle \rightarrow \langle S_1, H_1, \text{nr} \rangle \quad \langle S_1, H_1, \text{while } (e) \ c \rangle \rightarrow \langle S_2, H_2, v_r \rangle}{\langle S, H, \text{while } (e) \ c \rangle \rightarrow \langle S_2, H_2, v_r \rangle}$$

A.2 Semantics of Output Language

We present only the evaluation rules which involve regions. The others are similar the corresponding rules in the input language. The domains and domain equations for the operational semantics of the output language are:

$$\begin{aligned} \text{Value} &= \text{Integer} + \{\text{null}\} + \text{Location} \times \text{Region} \\ \text{Retval} &= \text{Value} + \{\text{nr}\} \\ \text{Stack} &= \text{Variable} \rightarrow \text{Value} \\ \text{Heap} &= \text{Location} \rightarrow (\text{Field} \rightarrow \text{Value}) \\ \text{RegEnv} &= \text{RegVar} \rightarrow \text{Region} \end{aligned}$$

Again, the value `nr` indicates that a command doesn't return.

A.2.1 Expressions

The semantics of expressions in the input language is defined using a relation $\langle S, H, R, \text{expr} \rangle \rightarrow v$ in the domain:

$$(\text{Stack} \times \text{Heap} \times \text{RegEnv} \times \text{Expr}) \rightarrow \text{Value}$$

The meaning of $\langle S, H, R, \text{expr} \rangle \rightarrow v$ is that, given a stack S , a heap H , and a region environment R , expression `expr` evaluates to value v . The rule for field access expressions is:

$$\frac{S(x) = (l, \rho) \quad \rho \in \text{range}(R) \quad H(l)(f) = v}{\langle S, H, R, x.f \rangle \rightarrow v}$$

A.2.2 Commands

We express the semantics of commands using a relation $\langle S, H, R, \text{com} \rangle \rightarrow \langle S', H', R', v \rangle$ in the domain:

$$\begin{aligned} &(\text{Stack} \times \text{Heap} \times \text{RegEnv} \times \text{Com}) \rightarrow \\ &\rightarrow (\text{Stack} \times \text{Heap} \times \text{RegEnv} \times \text{Retval}) \end{aligned}$$

The meaning of the relation $\langle S, H, R, \text{com} \rangle \rightarrow \langle S', H', R', v \rangle$ is that, given a stack S , a heap H , and a region environment R , the evaluation of command `com` yields a new stack S' , a new heap H' , a new region environment R' , and a result value v . Let $\text{range}(R)$ be the range of map R : $\text{range}(R) = \{y \mid \exists x. R(x) = y\}$. The evaluation rules are:

$$\frac{\rho \notin \text{range}(R)}{\langle S, H, R, \text{create } r \rangle \rightarrow \langle S, H, R \cup \{r \mapsto \rho\}, \text{nr} \rangle}$$

$$\frac{S'(x) = \begin{cases} \text{null} & \text{if } \text{reach}(x, S, H, \rho) \\ S(x) & \text{otherwise} \end{cases}}{\langle S, H, R \cup \{r \mapsto \rho\}, \text{remove } r \rangle \rightarrow \langle S', H, R, \text{nr} \rangle}$$

$$\frac{S(x) = (l, \rho) \quad \rho \in \text{range}(R) \quad \langle S, H, R, e \rangle \rightarrow v}{\langle S, H, R, x.f = e \rangle \rightarrow \langle S, H[l \mapsto H(l)[f \mapsto v]], R, \text{nr} \rangle}$$

$$\frac{R(r) = \rho \quad s : \text{ref}(\bar{t} \ \bar{f}) \quad m = \text{init}(\bar{t} \ \bar{f})}{\langle S, H, R, x = \text{new } s \text{ in } r \rangle \rightarrow \langle S[x \mapsto (l, \rho)], H \cup \{l \mapsto m\}, R, \text{nr} \rangle}$$

$$\frac{S(y) = v_1 \quad \mathbf{t} \ p[\bar{r}_p] \ (\mathbf{t} \ z) = \mathbf{c} \quad R' = R|_{\bar{r}} \quad \langle \{z \mapsto v_1\}, H, R', \mathbf{c} \ [\bar{r}/\bar{r}_p] \rangle \rightarrow \langle S_1, H_1, R', v \rangle \quad v \neq \mathbf{nr}}{\langle S, H, R, \mathbf{x} = p[\bar{r}] \ (y) \rangle \rightarrow \langle S[x \rightarrow v], H_1, R, \mathbf{nr} \rangle}$$

The initialization function init yields a map m such that:

$$m(f) = \begin{cases} 0 & \text{if } \text{int } f \in \bar{\mathbf{t}} \ \bar{f} \\ \text{null} & \text{if } \mathbf{s} \ f \in \bar{\mathbf{t}} \ \bar{f} \end{cases}$$

The $\text{reach}(\mathbf{x}, S, H, \rho)$ predicate indicates if a region ρ is reachable from a variable \mathbf{x} :

$$\frac{S(\mathbf{x}) = (l, \rho)}{(l, \rho) \in \text{reachLoc}(S, H)} \quad \frac{(l, \rho) \in \text{reachLoc}(S, H) \quad H(l)(f) = (l', \rho')}{(l', \rho') \in \text{reachLoc}(S, H)}$$

$$\text{reachReg}(S, H, \rho) = \exists l . (l, \rho) \in \text{reachLoc}(S, H)$$

$$\text{reach}(\mathbf{x}, S, H, \rho) = \text{reachReg}(\{\mathbf{x} \mapsto S(\mathbf{x})\}, H, \rho)$$

B Soundness

B.1 Partial Ordering

The partial ordering for types is the subtyping relation:

$$\frac{}{\perp <: \text{ref } [\bar{\rho}, \rho_0] \ (\bar{\tau} \ \bar{f})} \quad \frac{}{\tau <: \tau}$$

Type environments are ordered pointwise and empty environments are less than any other environments:

$$\frac{\text{dom}(\Gamma) = \text{dom}(\Gamma') \quad \forall \mathbf{x} . \Gamma(\mathbf{x}) <: \Gamma'(\mathbf{x})}{\Gamma \leq \Gamma'} \quad \frac{}{\emptyset \leq \Gamma}$$

The new environment computed by the unification function is the meet operator with respect to this ordering. That is, if $(\Gamma, E) = \text{unify}(\Gamma_1, \Gamma_2)$, then $\Gamma = \Gamma_1 \sqcap \Gamma_2$.

B.2 Live Regions

The live regions at a program point represent all of the regions that occur in the type environment at that point. We formally define live regions as follows.

Definition 1 *If τ is a type and \mathcal{T} is a translation of region variables to region names, then the regions of τ are:*

$$\begin{aligned} \text{Regions}(\text{int}) &= \text{Regions}(\text{null}) = \emptyset \\ \text{Regions}(\text{ref } [\rho_0, \bar{\rho}] \ (\bar{\tau} \ \bar{f}), \mathcal{T}) &= \{\mathcal{T}[\rho] \mid \rho \in (\bar{\rho} \cup \{\rho_0\})\} \end{aligned}$$

The set of live regions in Γ with respect to \mathcal{T} is:

$$\text{Live}(\Gamma, \mathcal{T}) = \bigcup_{x \in \text{dom}(\Gamma)} \text{Regions}(\Gamma(x), \mathcal{T})$$

With respect to the live sets, the meet operation translates into set union. That is, when $\Gamma = \Gamma_1 \sqcap \Gamma_2$, we have $\text{Live}(\Gamma) = \text{Live}(\Gamma_1) \cup \text{Live}(\Gamma_2)$.

B.3 Unification

Intuitively, the purpose of the unification function `unify` in the algorithm is to make its arguments comparable in the lattice. We formally define this function, as follows. We first define the function `unifyReg` which unifies regions and region sets:

$$\frac{\rho_1 \neq \rho_2}{\text{unifyReg}(\rho_1, \rho_2) = \{\rho_1 = \rho_2\}} \quad \frac{}{\text{unifyReg}(\rho, \rho) = \emptyset}$$

$$\frac{\forall i . E_i = \text{unifyReg}(\rho_i, \rho'_i)}{\text{unifyReg}(\rho_1 \dots \rho_n, \rho'_1 \dots \rho'_n) = E_1 \cup \dots \cup E_n}$$

Using this function, type unification is defined as follows:

$$\frac{}{\text{unify}(\text{int}, \text{int}) = (\text{int}, \emptyset)}$$

$$\frac{\tau = \text{ref } [\rho_0, \bar{\rho}] \ (\bar{\tau} \ \bar{\mathbf{f}})}{\text{unify}(\tau, \perp) = (\tau, \emptyset)} \quad \frac{\tau = \text{ref } [\rho_0, \bar{\rho}] \ (\bar{\tau} \ \bar{\mathbf{f}})}{\text{unify}(\perp, \tau) = (\tau, \emptyset)}$$

$$\frac{\begin{array}{l} \tau[\rho'_0/\rho_0, \bar{\rho}'/\bar{\rho}] = \tau' \\ \tau = \text{ref } [\rho_0, \bar{\rho}] \ (\bar{\tau} \ \bar{\mathbf{f}}) \quad \text{unifyReg}(\bar{\rho}, \bar{\rho}') = E \\ \tau' = \text{ref } [\rho'_0, \bar{\rho}'] \ (\bar{\tau}' \ \bar{\mathbf{f}}) \quad \text{unifyReg}(\rho_0, \rho'_0) = E_0 \end{array}}{\text{unify}(\tau, \tau') = (\tau, E_0 \cup E)}$$

$$\frac{\text{unify}(\tau_1, \tau'_1) = E_1 \quad \text{unify}(\tau_2, \tau'_2) = E_2}{\text{unify}(\tau_1 \rightarrow \tau_2, \tau'_1 \rightarrow \tau'_2) = (\tau_1 \rightarrow \tau_2, E_1 \cup E_2)}$$

Note that the analysis rules and unification function are such that bottom types (\perp) are never being used as arguments to function type constructors or reference type constructors.

B.4 Analysis Results

The region inference algorithm computes type information at each program point. Our type inference rules ensure that the following relations hold between the type environments at different points in the program.

$$\Gamma(\bullet(s \ x; c_1)) \cup \{x \mapsto \perp\} = \Gamma(\bullet c_1) \quad (1)$$

$$\Gamma((s \ x; c_1)\bullet) \cup \{x \mapsto \tau\} = \Gamma(c_1\bullet) \quad (2)$$

$$\Gamma(\bullet(\text{int } x; c_1)) \cup \{x \mapsto \text{int}\} = \Gamma(\bullet c_1) \quad (3)$$

$$\Gamma((\text{int } x; c_1)\bullet) \cup \{x \mapsto \tau\} = \Gamma(c_1\bullet) \quad (4)$$

$$\Gamma(\bullet(c_1; c_2)) = \Gamma(\bullet c_1) \quad (5)$$

$$\Gamma((c_1; c_2)\bullet) = \Gamma(c_2\bullet) \quad (6)$$

$$c_1; c_2 \Rightarrow \Gamma(c_1\bullet) = \Gamma(\bullet c_2) \quad (7)$$

$$\Gamma((\text{if } (\text{expr}) \text{ then } c_1 \text{ else } c_2)\bullet) = \Gamma(c_1\bullet) \sqcap \Gamma(c_2\bullet) \quad (8)$$

$$\Gamma(\bullet(\text{if } (\text{expr}) \text{ then } c_1 \text{ else } c_2)) = \Gamma(\bullet c_1) = \Gamma(\bullet c_2) \quad (9)$$

$$\Gamma(\bullet(\text{while } (\text{expr}) \ c_1)) \sqcap \Gamma(c_1\bullet) = \Gamma(\bullet c_1) \quad (10)$$

$$\Gamma((\text{while } (\text{expr}) \ c_1)\bullet) = \Gamma(\bullet c_1) \quad (11)$$

For assignments and return commands, the analysis result is such that the following relations hold:

$$c \equiv (x = e) : \Gamma(\bullet c) \vdash e : \tau \quad (12)$$

$$\Gamma(c\bullet) = \Gamma(\bullet c)[x \mapsto \tau] \quad (13)$$

$$c \equiv (x.f = e) : \Gamma(\bullet c) \vdash x.f : \tau \quad (14)$$

$$\Gamma(\bullet c) \vdash e : \tau', \tau' <: \tau \quad (15)$$

$$\Gamma(c\bullet) = \Gamma(\bullet c) \quad (16)$$

$$c \equiv (x = a : \text{new } s) : \Delta \vdash a : \tau \quad (17)$$

$$\Gamma(c\bullet) = \Gamma(\bullet c)[x \mapsto \tau] \quad (18)$$

$$c \equiv (x = cs : p(y)) : \Delta \vdash cs : \tau_1 \rightarrow \tau_2 \quad (19)$$

$$\Gamma \vdash y : \tau, \tau <: \tau_1 \quad (20)$$

$$\Gamma(c\bullet) = \Gamma(\bullet c)[x \mapsto \tau_2] \quad (21)$$

$$c \equiv (\text{return } x) : \Gamma \vdash \text{ret} : \tau \quad (22)$$

$$\Gamma \vdash x : \tau', \tau' <: \tau \quad (23)$$

$$\Gamma(c\bullet) = \emptyset \quad (24)$$

B.5 Translation

We give a full definition for the translation that occurs in the two phases of the algorithm. The translation \mathcal{T} uses the initialization environment Δ and the type environment Γ which contains type information at each program point. The translation for each procedure is:

$$\frac{\Delta \vdash p : \tau_1 \rightarrow \tau_2 \quad \text{FV}(\tau_1 \rightarrow \tau_2) = \bar{\rho}}{\mathcal{T}[\![t_2 \ p(t_1 \ z) = \text{com}]\!] \Delta \Gamma = \mathcal{T}[\![\tau_2]\!] \Delta \Gamma \ p \ [\bar{\rho}] \ (\mathcal{T}[\![\tau_1]\!] \Delta \Gamma \ z) = \mathcal{T}[\![\text{com}]\!] \Delta \Gamma} \quad (25)$$

At several points in our translation, the algorithm inserts sequences of **create** or **remove** commands. Given a set S of regions, we denote by **create** S the sequence of region creation commands for each region in S . Similarly, we denote by **remove** S the sequence of region removal commands for all regions in S . If $S = \emptyset$, these are empty sequences.

B.5.1 Control Flow Constructs

The translation for declarations and control flow constructs recurses on the structure of commands. At join points in the control flow it may create new regions; when variables go out of scope, the translation may remove existing regions. We define the translation $c_o = \mathcal{T}[\![c_i]\!]$ of an input command c_i into an output command c_o as follows:

$$\frac{\begin{array}{l} \mathcal{T}[\![c_{i1}]\!] \Delta\Gamma = c_{o1} \\ R_d = \text{Live}(c_{i1} \bullet) - \text{Live}(c_i \bullet) \end{array}}{\mathcal{T}[\![s \ x; c_{i1}]\!] \Delta\Gamma = (s \ x; c_{o1}); \text{remove } R_d} \quad (26)$$

$$\frac{\begin{array}{l} \mathcal{T}[\![c_{i1}]\!] \Delta\Gamma = c_{o1} \\ R_d = \text{Live}(c_{i1} \bullet) - \text{Live}(c_i \bullet) \end{array}}{\mathcal{T}[\![\text{int } x; c_{i1}]\!] \Delta\Gamma = (\text{int } x; c_{o1}); \text{remove } R_d} \quad (27)$$

$$\frac{\mathcal{T}[\![c_{i1}]\!] \Delta\Gamma = c_{o1} \quad \mathcal{T}[\![c_{i2}]\!] \Delta\Gamma = c_{o2}}{\mathcal{T}[\![c_{i1}; c_{i2}]\!] \Delta\Gamma = c_{o1}; c_{o2}} \quad (28)$$

$$\frac{\begin{array}{l} \mathcal{T}[\![c_{i1}]\!] \Delta\Gamma = c_{o1} \quad \mathcal{T}[\![c_{i2}]\!] \Delta\Gamma = c_{o2} \\ C_{c1} = \text{Live}(c_i \bullet) - \text{Live}(c_{i1} \bullet) \\ C_{c2} = \text{Live}(c_i \bullet) - \text{Live}(c_{i2} \bullet) \end{array}}{\mathcal{T}[\![\text{if } (e) \text{ then } c_{i1} \text{ else } c_{i2}]\!] \Delta\Gamma = \begin{array}{l} \text{if } (e) \text{ then } (c_{o1}; \text{create } C_{c1}) \\ \text{else } (c_{o2}; \text{create } C_{c2}) \end{array}} \quad (29)$$

$$\frac{\begin{array}{l} \mathcal{T}[\![c_{i1}]\!] \Delta\Gamma = c_{o1} \\ C_{w1} = \text{Live}(\bullet c_{i1}) - \text{Live}(\bullet c_i) \\ C_{w2} = \text{Live}(\bullet c_{i1}) - \text{Live}(c_{i1} \bullet) \end{array}}{\mathcal{T}[\![\text{while } (e) \ c_{i1}]\!] \Delta\Gamma = \begin{array}{l} \text{create } C_{w1}; \text{while } (e) \ (c_{o1}; \text{create } C_{w2}) \end{array}} \quad (30)$$

B.5.2 Basic Commands

The translation for basic commands (assignments and return commands) consists of two translations \mathcal{T}_a and \mathcal{T}_i . The former corresponds to the first phase of the algorithm, which adds region type annotations; the latter corresponds to the second phase, which inserts region creation and removal commands. The translation for basic commands is then the composition of the two:

$$\frac{\mathcal{T}_a[\![c]\!] \Delta\Gamma = c' \quad \mathcal{T}_i[\![c']]\! \Delta\Gamma = c''}{\mathcal{T}[\![c]\!] \Delta\Gamma = c''} \quad (31)$$

B.5.3 Region Type Annotations

We first define the translation for insertion of region annotations. Except for allocation commands and procedure calls, this is the identity function.

$$\frac{c \not\equiv (x = \text{new } c) \quad c \not\equiv (x = p(y))}{\mathcal{T}_a[\![c]\!] \Delta\Gamma = c} \quad (32)$$

$$\frac{c \equiv (x = a : \text{new } c) \quad \Delta \vdash a : \text{ref}[\rho_0, \bar{\rho}] \quad (\bar{\tau} \ \bar{f})}{\mathcal{T}_a[\![c]\!]\Delta\Gamma = x = \text{new } s \text{ in } \mathcal{T}[\![\rho_0]\!]} \quad (33)$$

$$\frac{c \equiv (x = c : p(y)) \quad \Delta \vdash c : \tau_1 \rightarrow \tau_2 \quad \bar{\rho} = \text{FV}(\tau_1) \cup \text{FV}(\tau_2)}{\mathcal{T}_a[\![x = p(y)]\!]\Delta\Gamma = x = p \ [\mathcal{T}[\![\rho]\!]] \ (y)} \quad (34)$$

B.5.4 Region Creation and Removal

The translation \mathcal{T}_a for inserting region creation and removal commands for basic statements is:

$$\frac{c \not\equiv (\text{return } x) \quad c \not\equiv (x = p[\bar{r}](y)) \quad \begin{array}{l} C_b = \text{Live}(c\bullet) - \text{Live}(\bullet c) \\ R_b = \text{Live}(\bullet c) - \text{Live}(c\bullet) \end{array}}{\mathcal{T}_i[\![c]\!]\Delta\Gamma = \text{create } C_b ; c ; \text{remove } R_b} \quad (35)$$

$$\frac{c \equiv (x = p[\bar{r}](y)) \quad \begin{array}{l} C_b = (\text{Live}(c\bullet) \cup \bar{r}) - \text{Live}(\bullet c) \\ R_b = \text{Live}(\bullet c) - \text{Live}(c\bullet) \end{array}}{\mathcal{T}_i[\![c]\!]\Delta\Gamma = \text{create } C_b ; c ; \text{remove } R_b} \quad (36)$$

$$\frac{c \equiv (\text{return } x) \quad R_r = \text{Live}(\bullet c)}{\mathcal{T}_i[\![c]\!]\Delta\Gamma = \text{remove } R_r ; c} \quad (37)$$

B.6 Consistency

We define the projection of entities from the output domains by removing all of their region information.

Definition 2 *If v is a variable in the output domain then the projection $v \downarrow$ is defined inductively as:*

$$v \downarrow = \begin{cases} v & \text{if } v = n \text{ or } v = \text{null} \text{ or } v = \text{nr} \\ l & \text{if } v = (l, r) \end{cases}$$

If S is a stack and H is a heap in the input domain, their projections $S \downarrow$ and $S \downarrow$ are defined by:

$$\begin{aligned} (S \downarrow)(v) &= (S(v)) \downarrow \\ (H \downarrow)(v)(f) &= (H(v)(f)) \downarrow \end{aligned}$$

We say that an output state (S_o, H_o) is homomorphic to an input state (S_i, H_i) , and write $(S_o, H_o) \sim (S_i, H_i)$, if $S_o \downarrow = S_i$ and $H_o \downarrow = H_i$. Similarly, $(S_o, H_o, v_o) \sim (S_i, H_i, v_i)$ if $S_o \downarrow = S_i$, $H_o \downarrow = H_i$, and $v_o \downarrow = v_i$.

Definition 3 *A configuration (S, H) is consistent with the type environment Γ , relative to a translation \mathcal{T} of region variables to region names, written $(S, H) \sim (\Gamma, \mathcal{T})$, if there exists a mapping μ of locations to reference types and regions $\mu(l) = (\tau, r)$, such that:*

$$\forall l . \mu(l) = (\text{ref}[\rho_0, \bar{\rho}] \ (\bar{\tau} \ \bar{f}), r) \wedge r = \mathcal{T}[\![\rho_0]\!] \quad (38)$$

$$S(x) = (l, r) \Rightarrow x : \tau \in \Gamma \wedge \mu(l) = (\tau, r) \quad (39)$$

$$\begin{aligned} H(l')(f) = (l, r) &\Rightarrow \mu(l') = (\text{ref}[\rho], \bar{\rho}) (\bar{\tau} \bar{f}), r') \wedge \\ &\mu(l) = (\tau, r) \wedge \tau f \in \bar{\tau} \bar{f} \end{aligned} \quad (40)$$

We use the shorthand notation $(v_o, H_o) \sim (\Gamma, \mathcal{T})$ for:

$$(\{\text{ret} \mapsto v_o\}, H_o) \sim (\Gamma, \mathcal{T})$$

Definition 4 We say that a state (S, H, R) in the output domain is consistent with a type environment Γ , relative to \mathcal{T} , and write $(S, H, R) \sim (\Gamma, \mathcal{T})$, if:

$$(S, H) \sim (\Gamma, \mathcal{T}) \quad (41)$$

$$\text{Live}(\Gamma, \mathcal{T}) = R \quad (42)$$

The following properties about the consistency between states and type information easily follow from the above definitions:

Property 1 If $(S, H) \sim (\Gamma, \mathcal{T})$ then:

$$\text{reachReg}(S, H, r) \Rightarrow r \in \text{Live}(\Gamma, \mathcal{T})$$

Property 2 If $(S, H, R) \sim (\Gamma, \mathcal{T})$ then:

$$\text{reachReg}(S, H, r) \Rightarrow r \in R$$

Property 3 If $(S, H, R) \sim (\Gamma, \mathcal{T})$ and $\Gamma \sqsubseteq \Gamma'$, then:

$$(S, H, R) \sim (\Gamma', \mathcal{T})$$

Property 4 If $(S, H, R) \sim (\Gamma, \mathcal{T})$, $\langle S, H, R, e \rangle \rightarrow (l, r)$, and $\Gamma \vdash e : \tau$, then:

$$\mu(l) = (\tau, r)$$

Property 5 If $(S, H, R) \sim (\Gamma, \mathcal{T})$ and $\Gamma \vdash e : \perp$, then:

$$\langle S, H, R, e \rangle \rightarrow \text{null}$$

B.7 Lemmas for Expressions

Lemma 1 (Expression Preservation) Given an expression e , we have that:

$$\langle S_i, H_i, e \rangle \rightarrow v_i \quad (43)$$

$$\langle S_o, H_o, R, e \rangle \rightarrow v_o \quad (44)$$

$$(S_o, H_o) \sim (S_i, H_i) \quad (45)$$

$$\Rightarrow v_o \downarrow = v_i \quad (46)$$

PROOF. We prove this lemma by induction on the derivations for $\langle S_i, H_i, e \rangle \rightarrow v_i$. We must examine each rule in the operational semantics for expressions.

Rule for $e = x$. Using relations (43) and (44) and the operational semantics of the input and output languages, we have: $S_i(x) = v_i$ and $S_o(x) = v_o$. Then relation (45) implies $v_o \downarrow = v_i$.

Rule for $e = n$. By the operational semantics of the input and output languages, we have: $v_i = n$ and $v_o = n$. By the definition of projection, $v_o \downarrow = v_i$.

Rule for $e = \text{null}$. By the operational semantics of the input and output languages, we have: $v_i = \text{null}$ and $v_o = \text{null}$. By the definition of projection, $v_o \downarrow = v_i$.

Rule for $e = e_1 \oplus e_2$. Because of relations (43) and (44) and the operational semantics of the input and output languages, we have:

$$\begin{aligned} \langle S_i, H_i, e_1 \rangle &\rightarrow n_1 & \langle S_o, H_o, e_1 \rangle &\rightarrow n'_1 \\ \langle S_i, H_i, e_2 \rangle &\rightarrow n_2 & \langle S_o, H_o, e_2 \rangle &\rightarrow n'_2 \end{aligned}$$

where $n_1 \oplus n_2 = n = v_i$ and $n'_1 \oplus n'_2 = n' = v_o$. Applying the induction hypothesis for e_1 and e_2 , we get: $n'_1 = n_1$ and $n'_2 = n_2$. Hence $n = n'$. Therefore, $v_o \downarrow = v_i$.

Rule for $e = x.f$. Using relations (43) and (44) and the operational semantics of the input and output languages, we have:

$$\begin{aligned} S_i(x) &= l & H_i(l)(f) &= v_i \\ S_o(x) &= (l', r) & H_o(l')(f) &= v_o & r \in R \end{aligned}$$

From relation (45) we have $S_o \downarrow = S_i$ and $H_o \downarrow = H_i$. Relation $S_o \downarrow = S_i$ implies $l = l'$. Relation $H_o \downarrow = H_i$ then implies $v_o \downarrow = v_i$. \square

Lemma 2 (Expression Progress) *Given an expression e , we have that:*

$$\langle S_i, H_i, e \rangle \rightarrow v_i \tag{47}$$

$$(S_o, H_o) \sim (S_i, H_i) \tag{48}$$

$$(S_o, H_o, R) \sim (\Gamma, \mathcal{T}) \tag{49}$$

\Rightarrow

$$\langle S_o, H_o, R, e \rangle \rightarrow v_o \tag{50}$$

PROOF. We prove this lemma by induction on the derivations for $\langle S_i, H_i, e \rangle \rightarrow v_i$. We must examine each rule in the operational semantics for expressions.

Rule for $e = x$. Because of relation (47), it means that $x \in \text{dom}(S_i)$. But $(S_o, H_o) \sim (S_i, H_i)$, so $x \in \text{dom}(S_o)$. Therefore, we can apply the rule for evaluation of variables in the output semantics, and get: $\langle S_o, H_o, R, x \rangle \rightarrow v_o$.

Rules for $e = n$ or $e = \text{null}$. The output operational semantics for integers and **null** are axioms, hence they apply directly: $\langle S_o, H_o, R, n \rangle \rightarrow n$ or $\langle S_o, H_o, R, \text{null} \rangle \rightarrow \text{null}$.

Rule for $e = e_1 \oplus e_2$. Using relation (47) and the operational semantics of the input language, we have:

$$\begin{aligned} \langle S_i, H_i, e_1 \rangle &\rightarrow n_1 \\ \langle S_i, H_i, e_2 \rangle &\rightarrow n_2 \end{aligned}$$

By induction hypothesis for e_1 and e_2 :

$$\begin{aligned} \langle S_o, H_o, R, e_1 \rangle &\rightarrow v_1 \\ \langle S_o, H_o, R, e_2 \rangle &\rightarrow v_2 \end{aligned}$$

By Expression Preservation Lemma, $v_1 = n_1$ and $v_2 = n_2$. Hence, we can apply the rule for evaluation of arithmetic expressions in the output language and get:

$$\langle S_o, H_o, R, e \rangle \rightarrow n$$

where $n = n_1 \oplus n_2$.

Rule for $e = x.f$. Using relation (47) and the operational semantics of the input language, we have:

$$S_i(x) = l \quad H_i(l)(f) = v_i$$

From relation (48) we have $S_o \downarrow = S_i$ and $H_o \downarrow = H_i$. Relation $S_o \downarrow = S_i$ implies $S_o(x) = (l, r)$. Relation $H_o \downarrow = H_i$ then implies $H_o(l)(f) = v_o$, with $v_o \downarrow = v_i$. Finally, relation (49) and Property 2 show that $r \in R$. We can apply the operational semantics for field accesses and evaluate the expression $x.f$: $\langle S_o, H_o, R, x.f \rangle \rightarrow v_o$. \square

B.8 Preservation Theorem

Theorem 1 (Preservation) *Given a command c_i in the input language, type environments $\Gamma(\bullet c_i)$ and $\Gamma(c_i \bullet)$ at program points before and after the command, and a translation \mathcal{T} , we have that:*

$$c_o = \mathcal{T}[\![c_i]\!] \tag{51}$$

$$\langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle \tag{52}$$

$$\langle S_o, H_o, R, c_o \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle \tag{53}$$

$$(S_o, H_o) \sim (S_i, H_i) \tag{54}$$

$$(S_o, H_o, R) \sim (\Gamma(\bullet c_i), \mathcal{T}) \tag{55}$$

\Rightarrow

$$(S'_o, H'_o, v_o) \sim (S'_i, H'_i, v_i) \tag{56}$$

$$(S'_o, H'_o, R') \sim (\Gamma(c_i \bullet), \mathcal{T}) \quad \text{if } v_i = \text{nr} \tag{57}$$

$$(v_o, H'_o) \sim (\Gamma(\bullet c_i), \mathcal{T}) \quad \text{if } v_i \neq \text{nr} \tag{58}$$

PROOF. The proof is by induction on the derivations of the evaluation $\langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$. We examine each of the rules in the operational semantics of the input language. We first prove the cases for declarations and control flow commands, and then prove the cases for basic commands.

Rule for $c_i = s \ x$. The translation of c_i is:

$$\begin{aligned} c_o &= (s \ x; c_{i1}); \text{remove } R_d \\ R_d &= \text{Live}(c_{i1} \bullet) - \text{Live}(c_i \bullet) \end{aligned}$$

Consider input and output states satisfying conditions (52)-(55). From the semantic rule considered in this case, we get:

$$\langle S_i \cup \{x \mapsto \text{null}\}, H_i, c_{i1} \rangle \rightarrow \langle S'_i \cup \{x \mapsto v_{xi}\}, H'_i, v_i \rangle$$

From condition (53) and the operational semantics of declarations, sequencing, and region removal in the output language, we have:

$$\begin{aligned} \langle S_o \cup \{x \mapsto \text{null}\}, H_o, R, c_{o1} \rangle &\rightarrow \langle S''_o \cup \{x \mapsto v_{xo}\}, H'_o, R'', v_o \rangle \\ R'' - R_d &= R' \end{aligned}$$

because the sequence of region removal commands $\text{remove } R_d$ only removes regions from R'' . Then, using Property 1 and the fact that R_d and $\text{Live}(\bullet c_i)$ are disjoint by definition of R_d , we conclude that $\neg \text{reach}(S'_o, H'_o, r)$ for each $r \in R_d$. Thus, the removal commands leaves the stack and heap unchanged, $S'_o = S''_o$.

Condition (54) implies:

$$(S_o \cup \{x \mapsto \text{null}\}, H_o) \sim (S_i \cup \{x \mapsto \text{null}\}, H_i)$$

Condition (55) implies:

$$(S_o \cup \{x \mapsto \text{null}\}, H_o, R) \sim (\Gamma(\bullet c_i) \cup \{x \mapsto \perp\}, T)$$

Analysis result relation (1) shows that: $\Gamma(\bullet c_i) \cup \{x \mapsto \perp\} = \Gamma(\bullet c_{i1})$. Therefore:

$$(S_o \cup \{x \mapsto \text{null}\}, H_o, R) \sim (\Gamma(\bullet c_{i1}), T)$$

All these equations show that conditions (52)-(55) are satisfied for the translation of the subcommand $c_{o1} = \mathcal{T}[\![c_{i1}]\!]$, and the derivation $\langle S_i \cup \{x \mapsto \text{null}\}, H_i, c_{i1} \rangle \rightarrow \langle S'_i \cup \{x \mapsto v_{xi}\}, H'_i, v_i \rangle$ is a sub-derivation of $\langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$. By induction hypothesis, we have:

$$\begin{aligned} (S'_o \cup \{x \mapsto v_{xo}\}, H'_o, v_o) &\sim (S'_i \cup \{x \mapsto v_{xi}\}, H'_i, v_i) \\ (S'_o \cup \{x \mapsto v_{xo}\}, H'_o, R'') &\sim (\Gamma(c_{i1} \bullet), T) \quad \text{if } v_i = \text{nr} \\ (v_o, H'_o) &\sim (\Gamma(\bullet c_{i1})(\text{ret}), T) \quad \text{if } v_i \neq \text{nr} \end{aligned}$$

Relations (1) and (2) show that the analysis result is such that: $\Gamma(\bullet c_{i1})(\text{ret}) = \Gamma(\bullet c_i)(\text{ret})$ and $\Gamma(c_{i1} \bullet) = \Gamma(c_i \bullet) \cup \{x \mapsto \tau\}$. Hence:

$$\begin{aligned} (S'_o \cup \{x \mapsto v_{xo}\}, H'_o, v_o) &\sim (S'_i \cup \{x \mapsto v_{xi}\}, H'_i, v_i) \\ (S'_o \cup \{x \mapsto v_{xo}\}, H'_o, R'') &\sim \\ &\sim (\Gamma(c_i \bullet) \cup \{x \mapsto \tau\}, T) \quad \text{if } v_i = \text{nr} \\ (v_o, H'_o) &\sim (\Gamma(\bullet c_i)(\text{ret}), T) \quad \text{if } v_i \neq \text{nr} \end{aligned}$$

The last relation above is precisely condition (58). The first relation above implies condition (56):

$$(S'_o, H'_o, v_o) \sim (S'_i, H'_i, v_i)$$

Finally, to show condition (57), we prove that:

$$(S'_o \cup \{x \mapsto v_{xo}\}, H'_o, R'') \sim (\Gamma(c_i \bullet) \cup \{x \mapsto \tau\}, T)$$

implies:

$$(S'_o, H'_o, R') \sim (\Gamma(c_i \bullet), T)$$

For this, we inspect the conditions from the definition of consistency. Consistency condition (41) immediately follow from the former consistency relation because the stacks and the type environments in the two relations are identical except for x . For condition (42), the former consistency relation shows that: $R'' = \text{Live}(c_{i1} \bullet)$, since $\Gamma(c_{i1} \bullet) = \Gamma(c_i \bullet) \cup \{x \mapsto \tau\}$. Hence:

$$\begin{aligned} R' &= R'' - R_d \\ &= \text{Live}(c_{i1} \bullet) - (\text{Live}(c_{i1} \bullet) - \text{Live}(c_i \bullet)) \\ &= \text{Live}(c_i \bullet) \end{aligned}$$

The above relation holds because $\text{Live}(c_i \bullet) \subseteq \text{Live}(c_{i1} \bullet)$. We have therefore proved all the conditions for the consistency relation $(S'_o, H'_o, R') \sim (\Gamma(c_i \bullet), T)$, which completes the proof in this case.

Rule for $\mathbf{c_i} = \mathbf{int\ x}$. The proof is similar to the case above.

Rule 1 for $\mathbf{c_i} = \mathbf{c_{i1}} ; \mathbf{c_{i2}}$. Let $\mathbf{c_{o1}} = \mathcal{T}[\llbracket \mathbf{c_{i1}} \rrbracket]$ and $\mathbf{c_{o2}} = \mathcal{T}[\llbracket \mathbf{c_{i2}} \rrbracket]$. The translation of $\mathbf{c_i}$ is $\mathbf{c_o} = \mathbf{c_{o1}} ; \mathbf{c_{o2}}$. Consider input and output states satisfying conditions (52)-(55). From the semantic rule considered in this case, we have:

$$\langle S_i, H_i, \mathbf{c_{i1}} \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle \quad v_i \neq \mathbf{nr}$$

From condition (52), one of the two rules for sequencing in the output language applies. Both of these evaluate the first command in the sequence:

$$\langle S_o, H_o, R, \mathbf{c_{o1}} \rangle \rightarrow \langle S''_o, H''_o, R'', v''_o \rangle$$

From condition (54), $(S_o, H_o) \sim (S_i, H_i)$. We also have $\Gamma(\bullet \mathbf{c_i}) = \Gamma(\bullet \mathbf{c_{i1}})$, from (5), so $(S_o, H_o, R) \sim (\Gamma(\bullet \mathbf{c_{i1}}), T)$. All conditions (52)-(55) are satisfied for the translation of $\mathbf{c_{i1}}$, with the evaluation $\langle S_i, H_i, \mathbf{c_{i1}} \rangle \rightarrow \langle S'_i, H'_i, \mathbf{nr} \rangle$. Thus, we can apply the induction hypothesis for the translation of $\mathbf{c_{i1}}$. By induction hypothesis, we get $v''_o \downarrow = \mathbf{nr}$, so $v''_o = \mathbf{nr}$. Therefore, we must also use the first sequencing rule in the output semantics:

$$\langle S_o, H_o, R, \mathbf{c_{o1}} \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle$$

Because the evaluation of $\mathbf{c_{i1}}$ returns, we directly use the conclusion of relation (58). By induction hypothesis for the translation of $\mathbf{c_{i1}}$, we have:

$$\begin{aligned} (S'_o, H'_o, v_o) &\sim (S'_i, H'_i, v_i) \\ (v_o, H'_o) &\sim (\Gamma(\bullet \mathbf{c_{i1}}), T) \end{aligned}$$

This completes the proof in this case, since $\Gamma(\bullet \mathbf{c_i}) = \Gamma(\bullet \mathbf{c_{i1}})$, by relation (5), and the type of \mathbf{ret} is the same at all program points: $\Gamma(\mathbf{c_i} \bullet) = \Gamma(\mathbf{c_{i2}} \bullet)$.

Rule 2 for $\mathbf{c_i} = \mathbf{c_{i1}} ; \mathbf{c_{i2}}$. Let $\mathbf{c_{o1}} = \mathcal{T}[\llbracket \mathbf{c_{i1}} \rrbracket]$ and $\mathbf{c_{o2}} = \mathcal{T}[\llbracket \mathbf{c_{i2}} \rrbracket]$. The translation of $\mathbf{c_i}$ is $\mathbf{c_o} = \mathbf{c_{o1}} ; \mathbf{c_{o2}}$. Consider input and output states satisfying conditions (52)-(55). From the semantic rule considered in this case, we have:

$$\begin{aligned} \langle S_i, H_i, \mathbf{c_{i1}} \rangle &\rightarrow \langle S''_i, H''_i, \mathbf{nr} \rangle \\ \langle S''_i, H''_i, \mathbf{c_{i2}} \rangle &\rightarrow \langle S'_i, H'_i, v_i \rangle \end{aligned}$$

From condition (52), one of the two rules for sequencing in the output language applies. Both of these evaluate the first command in the sequence:

$$\langle S_o, H_o, R, \mathbf{c_{o1}} \rangle \rightarrow \langle S''_o, H''_o, R'', v''_o \rangle$$

From condition (54), $(S_o, H_o) \sim (S_i, H_i)$. We also have $\Gamma(\bullet \mathbf{c_i}) = \Gamma(\bullet \mathbf{c_{i1}})$, from (5), so $(S_o, H_o, R) \sim (\Gamma(\bullet \mathbf{c_{i1}}), T)$.

All conditions (52)-(55) are satisfied for the translation of $\mathbf{c_{i1}}$, with the evaluation $\langle S_i, H_i, \mathbf{c_{i1}} \rangle \rightarrow \langle S''_i, H''_i, \mathbf{nr} \rangle$. Thus, we can apply the induction hypothesis for the translation of $\mathbf{c_{i1}}$. By induction hypothesis, we get $v''_o \downarrow = \mathbf{nr}$, so $v''_o = \mathbf{nr}$. Therefore, we must also use the second sequencing rule in the output semantics:

$$\langle S''_o, H''_o, R'', \mathbf{c_{o2}} \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle$$

Because the evaluation of $\mathbf{c_{i1}}$ doesn't return, we directly use the conclusion of relation (57). By induction hypothesis for the translation of $\mathbf{c_{i1}}$, we have:

$$\begin{aligned} (S''_o, H''_o, \mathbf{nr}) &\sim (S''_i, H''_i, \mathbf{nr}) \\ (S''_o, H''_o, R'') &\sim (\Gamma(\mathbf{c_{i1}} \bullet), T) \end{aligned}$$

The analysis result is such that $\Gamma(\mathbf{c}_{i1}\bullet) = \Gamma(\bullet\mathbf{c}_{i2})$, by relation (7). Thus, all conditions (52)-(55) are satisfied for the translation of \mathbf{c}_{i2} , with the evaluation $\langle S''_i, H''_i, \mathbf{c}_{i2} \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$. By induction hypothesis we get:

$$\begin{aligned} (S'_o, H'_o, v_o) &\sim (S'_i, H'_i, v_i) \\ (S'_o, H'_o, R') &\sim (\Gamma(\mathbf{c}_{i2}\bullet), \mathcal{T}) \quad \text{if } v_i = \text{nr} \\ (v_o, H'_o) &\sim (\Gamma(\bullet\mathbf{c}_{i2})(\text{ret}), \mathcal{T}) \quad \text{if } v_i \neq \text{nr} \end{aligned}$$

This completes the proof in this case, since $\Gamma(\mathbf{c}_i\bullet) = \Gamma(\mathbf{c}_{i2}\bullet)$, by relation (6), and the type of ret is the same at all program points: $\Gamma(\mathbf{c}_i\bullet) = \Gamma(\mathbf{c}_{i2}\bullet)$.

Rule 1 for $\mathbf{c}_i = \text{if } (e) \text{ then } \mathbf{c}_{i1} \text{ else } \mathbf{c}_{i2}$. This rule evaluates the true branch \mathbf{c}_{i1} of the command. Let $\mathbf{c}_{o1} = \mathcal{T}[\![\mathbf{c}_{i1}]\!]$ and $\mathbf{c}_{o2} = \mathcal{T}[\![\mathbf{c}_{i2}]\!]$. The translation of \mathbf{c}_i is:

$$\mathbf{c}_o = \text{if } (e) \text{ then } (\mathbf{c}_{o1} ; \text{create } C_{c1}) \text{ else } (\mathbf{c}_{o2} ; \text{create } C_{c2})$$

$$\begin{aligned} \text{where: } C_{c1} &= \text{Live}(\mathbf{c}_i\bullet) - \text{Live}(\mathbf{c}_{i1}\bullet) \\ C_{c2} &= \text{Live}(\mathbf{c}_i\bullet) - \text{Live}(\mathbf{c}_{i2}\bullet) \end{aligned}$$

Consider input and output states satisfying conditions (52)-(55). From the semantic rule considered in this case, we have:

$$\begin{aligned} \langle S_i, H_i, e \rangle &\rightarrow v \notin \{0, \text{null}\} \\ \langle S_i, H_i, \mathbf{c}_{i1} \rangle &\rightarrow \langle S'_i, H'_i, v_i \rangle \end{aligned}$$

From condition (53) and the operational semantics of the output language, e must evaluate to a value (since both rules have the evaluation of e in the premise):

$$\langle S_o, H_o, R, e \rangle \rightarrow v'$$

Because consistency relation (54), we can apply the Expression Preservation Lemma, and get that $v' \downarrow = v$. But $v \notin \{0, \text{null}\}$. Hence, $v' \notin \{0, \text{null}\}$. Hence, the first rule for conditionals in the output semantics must apply:

$$\langle S_o, H_o, R, \mathbf{c}_{o1} ; \text{create } C_{c1} \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle$$

Therefore:

$$\begin{aligned} \langle S_o, H_o, R, \mathbf{c}_{o1} \rangle &\rightarrow \langle S'_o, H'_o, R'', v_o \rangle \\ R'' \cup C_{c1} &= R' \end{aligned}$$

By condition (54): $(S_o, H_o) \sim (S_i, H_i)$. By condition (55) and analysis result relation (9): $(S_o, H_o, R) \sim (\Gamma(\bullet\mathbf{c}_{i1}), \mathcal{T})$.

All conditions (52)-(55) are satisfied for the translation of \mathbf{c}_{i1} , with the evaluation $\langle S_i, H_i, \mathbf{c}_{i1} \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$. By induction hypothesis we get:

$$\begin{aligned} (S'_o, H'_o, v_o) &\sim (S'_i, H'_i, v_i) \\ (S'_o, H'_o, R'') &\sim (\Gamma(\mathbf{c}_{i1}\bullet), \mathcal{T}) \quad \text{if } v_i = \text{nr} \\ (v_o, H'_o) &\sim (\Gamma(\bullet\mathbf{c}_{i1})(\text{ret}), \mathcal{T}) \quad \text{if } v_i \neq \text{nr} \end{aligned}$$

The first relation above is precisely relation (56). The last relation above implies relation (58), since the return type is the same at all program points. Finally, to prove relation (57), we show that:

$$(S'_o, H'_o, R'') \sim (\Gamma(\mathbf{c}_{i1}\bullet), \mathcal{T})$$

implies:

$$(S'_o, H'_o, R') \sim (\Gamma(\mathbf{c}_i\bullet), \mathcal{T})$$

For this, we inspect the conditions from the definition of consistency. By the analysis result relation (8) we have that $\Gamma(\mathbf{c}_{i1}\bullet) \subseteq \Gamma(\mathbf{c}_i\bullet)$. By Property 3, consistency condition (41) holds at program point $\mathbf{c}_i\bullet$. For condition (42), the consistency relation $(S'_o, H'_o, R'') \sim (\Gamma(\mathbf{c}_{i1}\bullet), \mathcal{T})$ shows that: $R'' = \text{Live}(\mathbf{c}_{i1}\bullet)$. Hence:

$$\begin{aligned} R' &= R'' \cup C_{c1} \\ &= \text{Live}(\mathbf{c}_{i1}\bullet) \cup (\text{Live}(\mathbf{c}_i\bullet) - \text{Live}(\mathbf{c}_{i1}\bullet)) \\ &= \text{Live}(\mathbf{c}_i\bullet) \end{aligned}$$

The above relation holds because $\text{Live}(\mathbf{c}_{i1}\bullet) \subseteq \text{Live}(\mathbf{c}_i\bullet)$. We have therefore proved all the conditions for the consistency relation $(S'_o, H'_o, R') \sim (\Gamma(\mathbf{c}_i\bullet), \mathcal{T})$, which completes the proof in this case.

Rule 2 for $\mathbf{c}_i = \text{if (e) then } \mathbf{c}_{i1} \text{ else } \mathbf{c}_{i2}$. This rule evaluates the false branch \mathbf{c}_{i2} of the command and the proof is similar to the previous case.

Rules for $\mathbf{c}_i = \text{while (e) } \mathbf{c}_{i1}$. We consider all of the three rules for the evaluation of while loops. The conclusion of all of these rules is:

$$(S_i, H_i, \text{while (e) } \mathbf{c}_{i1}) \rightarrow (S'_i, H'_i, v_i)$$

Let $\mathbf{c}_{o1} = \mathcal{T}[\![\mathbf{c}_{i1}]\!]$. The translation of \mathbf{c}_i is:

$$\mathbf{c}_o = \text{create } C_{w1} ; \text{while (e) } (\mathbf{c}_{o1} ; \text{create } C_{w2})$$

$$\begin{aligned} \text{where: } C_{w1} &= \text{Live}(\bullet\mathbf{c}_{i1}) - \text{Live}(\bullet\mathbf{c}_i) \\ C_{w2} &= \text{Live}(\bullet\mathbf{c}_{i1}) - \text{Live}(\mathbf{c}_{i1}\bullet) \end{aligned}$$

Consider input and output states satisfying conditions (52)-(55). From condition (53), the output command first executes the region creation commands $\text{create } C_{w1}$, and then evaluates the loop. If we denote by $\mathbf{c}'_{o1} = (\mathbf{c}_{o1} ; \text{create } C_{w2})$, we have:

$$\begin{aligned} \langle S_o, H_o, R, \text{create } C_{w1} \rangle &\rightarrow \langle S_o, H_o, R_{w1}, \text{nr} \rangle \\ \langle S_o, H_o, R_{w1}, \text{while (e) } \mathbf{c}'_{o1} \rangle &\rightarrow \langle S'_o, H'_o, R', v_o \rangle \\ \text{where: } R_{w1} &= R \cup C_{w1} \end{aligned}$$

By induction hypothesis, each sub-derivation of the evaluation $\langle S_i, H_i, \mathbf{c}_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$ satisfies the implication in the theorem. These sub-derivations include the evaluation of \mathbf{c}_{i1} in the second and the third rule for loops. Given this fact, the following property holds:

Property 6 *Consider the commands \mathbf{c}_{i1} and \mathbf{c}'_{o1} in the current case. Let $(S_{pi}, H_{pi}), (S'_{pi}, H'_{pi}, v_i)$ $(S_{po}, H_{po}, R_p), (S'_{po}, H'_{po}, R'_p, v_o)$ be states and values such that:*

$$\langle S_{pi}, H_{pi}, \text{while (e) } \mathbf{c}_{i1} \rangle \rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle \quad (59)$$

$$\langle S_{po}, H_{po}, R_p, \text{while (e) } \mathbf{c}'_{o1} \rangle \rightarrow \langle S'_{po}, H'_{po}, R'_p, v_{po} \rangle \quad (60)$$

$$(S_{po}, H_{po}) \sim (S_i, H_i) \quad (61)$$

$$(S_{po}, H_{po}, R_p) \sim (\Gamma(\bullet\mathbf{c}_{i1}), \mathcal{T}) \quad (62)$$

$$(63)$$

and such that the derivation for:

$$(S_{pi}, H_{pi}, \text{while (e) } \mathbf{c}_{i1}) \rightarrow (S'_{pi}, H'_{pi}, v_{pi})$$

is a sub-derivation of, or is the same as, the derivation for:

$$(S_i, H_i, \text{while } (e) \text{ } c_{i1}) \rightarrow (S'_i, H'_i, v_i)$$

Then, the following relations hold:

$$(\mathcal{S}'_{po}, H'_{po}, v_{po}) \sim (S'_i, H'_i, v_i) \quad (64)$$

$$(S'_{po}, H'_{po}, R'_p) \sim (\Gamma(\bullet c_{i1}), \mathcal{T}) \quad \text{if } v_{pi} = \text{nr} \quad (65)$$

$$(v_{po}, H'_{po}) \sim (\Gamma(\bullet c_{i1})(\text{ret}), \mathcal{T}) \quad \text{if } v_{pi} \neq \text{nr} \quad (66)$$

In other words, if we start with a output state consistent with the type information at the top of the loop body (i.e., program point $\bullet c_{i1}$), then the execution of the loop preserves this invariants.

Using this result, we can easily prove the relations required for this case, using the following instantiation:

$$\begin{aligned} (S_{pi}, H_{pi}) &= (S_i, H_i) & (S'_{pi}, H'_{pi}) &= (S'_i, H'_i) & v_{pi} &= v_i \\ (S_{po}, H_{po}) &= (S_o, H_o) & (S'_{po}, H'_{po}) &= (S'_o, H'_o) & v_{po} &= v_o \\ R_p &= R_{w1} & R'_p &= R' \end{aligned}$$

With these substitutions, relations (59)-(61) immediately follow from (52)-(54). We can also prove relation (62) using condition (55), as follows. We inspect the conditions from the definition of consistency. By the analysis result relation (10) we have that $\Gamma(\bullet c_i) \subseteq \Gamma(\bullet c_{i1})$. By Property 3 and relation (55), consistency condition (41) holds at program point $\bullet c_{i1}$. Finally, consistency condition (42) holds because:

$$\begin{aligned} R_{w1} &= R \cup C_{w1} \\ &= \text{Live}(\bullet c_i) \cup C_{w1} && \text{by condition (55)} \\ &= \text{Live}(\bullet c_i) \cup \\ &\quad \cup (\text{Live}(\bullet c_{i1}) - \text{Live}(\bullet c_i)) && \text{by definition of } C_{w1} \\ &= \text{Live}(\bullet c_{i1}) && \text{Live}(\bullet c_i) \subseteq \text{Live}(\bullet c_{i1}) \\ &&& \text{by relation (10)} \end{aligned}$$

We have therefore proved all the conditions for the consistency relation $(S_o, H_o, R_{w1}) \sim (\Gamma(\bullet c_{i1}), \mathcal{T})$. Hence, relation (62) also holds. We now apply the above property and get:

$$\begin{aligned} (S'_o, H'_o, v_o) &\sim (S'_i, H'_i, v_i) \\ (S'_o, H'_o, R') &\sim (\Gamma(\bullet c_{i1}), \mathcal{T}) \quad \text{if } v_i = \text{nr} \\ (v_o, H'_o) &\sim (\Gamma(\bullet c_{i1})(\text{ret}), \mathcal{T}) \quad \text{if } v_i \neq \text{nr} \end{aligned}$$

Since the analysis result is such that $\Gamma(\bullet c_{i1}) = \Gamma(c_i \bullet)$, by relation (11), and $\Gamma(\bullet c_{i1})(\text{ret}) = \Gamma(\bullet c_i)(\text{ret})$, this proves the relations for this case.

We now prove the property by induction on the derivations of the evaluation $\langle S_{pi}, H_{pi}, \text{while } (e) \text{ } c_{i1} \rangle \rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle$. Consider input and output states and values satisfying relations (59) - (62). From condition (59), one of the three rules for while loops in the input operational semantics must apply. We consider each of these rules in turn.

Subcase: While Rule 1. Assume the first rule for while loops applies:

$$\begin{aligned} \langle S_{pi}, H_{pi}, e \rangle &\rightarrow v \quad v \in \{0, \text{null}\} \\ S'_{pi} &= S_{pi} \quad H'_{pi} = H_{pi} \quad v_{pi} = \text{nr} \end{aligned}$$

Next consider the evaluation $\langle S_{po}, H_{po}, R_p, \text{while } (e) \ c'_{o1} \rangle \rightarrow \langle S'_{po}, H'_{po}, R', v_{po} \rangle$. Because of the evaluation of $\text{while } (e) \ c'_{o1}$, one of the three rules for while loops in the input operational semantics must apply. Each of these rules evaluates the expression e :

$$\langle S_{po}, H_{po}, R_p, e \rangle \rightarrow v'$$

By the Expression Preservation Lemma, $v' \downarrow = v$. But $v \in \{0, \text{null}\}$, therefore $v' \in \{0, \text{null}\}$. Hence, the first rule for while loops in the output operational semantics also applies. Therefore:

$$S'_{po} = S_{po} \quad H'_{po} = H_{po} \quad R'_p = R_p \quad v_{po} = \text{nr}$$

From condition (61), we get: $(S'_{po}, H'_{po}, v_{po}) \sim (S'_{pi}, H'_{pi}, v_{pi})$. Finally, because $v_{pi} = \text{nr}$, we need only to check relation (65). This relation immediately follows from (62), because $S'_{po} = S_{po}$, $H'_{po} = H_{po}$, and $R'_p = R_p$. This completes the proof for this subcase.

Subcase: While Rule 2. Assume the second rule for while loops applies:

$$\begin{aligned} \langle S_{pi}, H_{pi}, e \rangle &\rightarrow v \quad v \notin \{0, \text{null}\} \\ \langle S_{pi}, H_{pi}, c_{i1} \rangle &\rightarrow \langle S_{pi}, H_{pi}, v_{pi} \rangle \\ S'_{pi} &= S_{pi} \quad H'_{pi} = H_{pi} \quad v_{pi} \neq \text{nr} \end{aligned}$$

Next consider the evaluation $\langle S_{po}, H_{po}, R_p, \text{while } (e) \ c'_{o1} \rangle \rightarrow \langle S'_{po}, H'_{po}, R', v_{po} \rangle$. Because of the evaluation of $\text{while } (e) \ c'_{o1}$, one of the three rules for while loops in the input operational semantics must apply. Each of these rules evaluates the expression e :

$$\langle S_{po}, H_{po}, R_p, e \rangle \rightarrow v'$$

By the Expression Preservation Lemma, $v' \downarrow = v$. But $v \notin \{0, \text{null}\}$, therefore $v' \notin \{0, \text{null}\}$. Hence, one of the last two rules for while loops in the output operational semantics applies. Each of these rules evaluates the loop body c'_{o1} . This first evaluates c_{o1} :

$$\langle S_{po}, H_{po}, R_p, c_{o1} \rangle \rightarrow \langle S''_{po}, H''_{po}, R''_p, v'_{po} \rangle$$

From condition (61), $(S_{po}, H_{po}) \sim (S_{pi}, H_{pi})$. From condition (62), $(S_{po}, H_{po}, R_p) \sim (\Gamma(\bullet c_{i1}), T)$. We can therefore apply the *outer induction hypothesis*, for the translation of c_{i1} with the evaluation $\langle S_{pi}, H_{pi}, c_{i1} \rangle \rightarrow \langle S_{pi}, H_{pi}, v_{pi} \rangle$, because we know that its derivation is a sub-derivation of $(S_i, H_i, \text{while } (e) \ c_{i1}) \rightarrow (S'_i, H'_i, v_i)$. Since $v_{pi} \neq \text{nr}$, we get:

$$\begin{aligned} (S''_{po}, H''_{po}, v'_{po}) &\sim (S'_{pi}, H'_{pi}, v_{pi}) \\ (v'_{po}, H''_{po}) &\sim (\Gamma(\bullet c_{i1})(\text{ret}), T) \end{aligned}$$

Hence, $v'_{po} \downarrow = v_{pi}$. But $v_{pi} \neq \text{nr}$. So $v'_{po} \neq \text{nr}$. Therefore, the evaluation of the transformed program also uses the second rule for while loops (but from the output operational semantics):

$$S''_{po} = S'_{po} \quad H''_{po} = H'_{po} \quad R''_p = R'_p \quad v'_{po} = v_{po}$$

We then substitute these values in the result of the outer induction hypothesis:

$$\begin{aligned} (S'_{po}, H'_{po}, v_{po}) &\sim (S'_{pi}, H'_{pi}, v_{pi}) \\ (v_{po}, H'_{po}) &\sim (\Gamma(\bullet c_{i1})(\text{ret}), T) \end{aligned}$$

Along with the fact that $v_{po} \neq \text{nr}$, this completes the proof for this subcase.

Subcase: While Rule 3. Assume the third rule for while loops applies:

$$\begin{aligned}\langle S_{pi}, H_{pi}, e \rangle &\rightarrow v \quad v \notin \{0, \text{null}\} \\ \langle S_{pi}, H_{pi}, c_{i1} \rangle &\rightarrow \langle S''_{pi}, H''_{pi}, \text{nr} \rangle \\ \langle S''_{pi}, H''_{pi}, c_i \rangle &\rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle\end{aligned}$$

Next consider the evaluation $\langle S_{po}, H_{po}, R_p, \text{while } (e) \text{ } c'_{o1} \rangle \rightarrow \langle S'_{po}, H'_{po}, R', v_{po} \rangle$. Because of this evaluation, one of the three rules for while loops in the input operational semantics must apply. Each of these rules evaluates the expression e :

$$\langle S_{po}, H_{po}, R_p, e \rangle \rightarrow v'$$

By the Expression Preservation Lemma, $v' \downarrow = v$. But $v \notin \{0, \text{null}\}$, therefore $v' \notin \{0, \text{null}\}$. Hence, one of the last two rules for while loops in the output operational semantics applies. Each of these rules evaluates the loop body c'_{o1} . This first evaluates c_{o1} :

$$\langle S_{po}, H_{po}, R_p, c_{o1} \rangle \rightarrow \langle S''_{po}, H''_{po}, R'_p, v'_{po} \rangle$$

From condition (61), $(S_o, H_o) \sim (S_i, H_i)$. From condition (62), $(S_{po}, H_{po}, R_p) \sim (\Gamma(\bullet c_{i1}), T)$. We can apply the *outer induction hypothesis*, for the translation of c_{i1} with the evaluation $\langle S_{pi}, H_{pi}, c_{i1} \rangle \rightarrow \langle S''_{pi}, H''_{pi}, \text{nr} \rangle$. Using the fact that c_{i1} yields a nr value, we get:

$$\begin{aligned}(S''_{po}, H''_{po}, v'_{po}) &\sim (S'_{pi}, H'_{pi}, v_{pi}) \\ (S''_{po}, H''_{po}, R'_p) &\sim (\Gamma(c_{i1} \bullet), T)\end{aligned}\tag{67}$$

Hence, $v'_{po} \downarrow = \text{nr}$. So $v'_{po} = \text{nr}$. Therefore, the evaluation of the transformed program uses the third rule for while loops in the output operational semantics:

$$\begin{aligned}\langle S_{po}, H_{po}, R_p, c'_{o1} \rangle &\rightarrow \langle S''_{po}, H''_{po}, R'_p \cup C_{w2}, \text{nr} \rangle \\ \langle S''_{po}, H''_{po}, R'_p \cup C_{w2}, \text{while } (e) \text{ } c'_{o1} \rangle &\rightarrow \langle S'_{po}, H'_{po}, R'_p, v_{po} \rangle\end{aligned}$$

because the evaluation of c'_{o1} consists of the evaluation of c_{o1} followed by the sequence of region commands $\text{create } C_{w2}$. We know that $(S''_{po}, H''_{po}) \sim (S'_{pi}, H'_{pi})$. We can also prove that $(S''_{po}, H''_{po}, R'_p \cup C_{w2}) \sim (\Gamma(\bullet c_{i1}), T)$, as follows. We inspect the conditions from the definition of consistency. By the analysis result relation (10) we have that $\Gamma(c_{i1} \bullet) \sqsubseteq \Gamma(\bullet c_{i1})$. By Property 3 and above relation (67), consistency condition (41) holds at program point $\bullet c_{i1}$. And condition (42) holds because:

$$\begin{aligned}R'_p \cup C_{w2} &= \\ &= \text{Live}(c_{i1} \bullet) \cup C_{w2} && \text{by relation (67)} \\ &= \text{Live}(c_{i1} \bullet) \cup \\ &\quad \cup (\text{Live}(\bullet c_{i1}) - \text{Live}(c_{i1} \bullet)) && \text{by definition of } C_{w2} \\ &= \text{Live}(\bullet c_{i1}) && \text{Live}(c_{i1} \bullet) \sqsubseteq \text{Live}(\bullet c_{i1}) \\ &&& \text{by relation (10)}\end{aligned}$$

We have therefore proved all the conditions for the consistency relation $(S''_{po}, H''_{po}, R'_p \cup C_{w2}) \sim (\Gamma(\bullet c_{i1}), T)$. We can now apply the *inner induction hypothesis* for the evaluations:

$$\begin{aligned}\langle S''_{pi}, H''_{pi}, c_i \rangle &\rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle \\ \langle S''_{po}, H''_{po}, R'_p \cup C_{w2}, \text{while } (e) \text{ } c'_{o1} \rangle &\rightarrow \langle S'_{po}, H'_{po}, R'_p, v_{po} \rangle\end{aligned}$$

By the inner induction hypothesis we get:

$$\begin{aligned}(S'_{po}, H'_{po}, v_{po}) &\sim (S'_{pi}, H'_{pi}, v_{pi}) \\ (S'_{po}, H'_{po}, R'_p) &\sim (\Gamma(\bullet c_{i1}), T) \quad \text{if } v_{pi} = \text{nr} \\ (v_{po}, H'_{po}) &\sim (\Gamma(\bullet c_{i1})(\text{ret}), T) \quad \text{if } v_{pi} \neq \text{nr}\end{aligned}$$

Finally, because $\Gamma(\bullet c_{i1}) = \Gamma(c_i \bullet)$ by relation (11), and because $\Gamma(\bullet c_{i1})(\text{ret}) = \Gamma(\bullet c_i)(\text{ret})$, this completes the proof for this subcase and the proof of the property.

Rule for $c_i = (x = e)$. The translation of c_i is:

$$\begin{aligned} c_o &= \text{create } C_b ; c_i ; \text{remove } R_b \\ \text{where } R_b &= \text{Live}(\bullet c_i) - \text{Live}(c_i \bullet) \\ C_b &= \text{Live}(c_i \bullet) - \text{Live}(\bullet c_i) \end{aligned}$$

Consider input and output states satisfying conditions (52)-(55). From the semantic rules of assignments in the input and output languages, we have:

$$\begin{aligned} x \in \text{dom}(S_i) \quad \langle S_i, H_i, e \rangle &\rightarrow v \\ S'_i = S_i[x \rightarrow v] \quad H'_i = H_i \quad v_i = \text{nr} \\ \\ x \in \text{dom}(S_o) \quad \langle S_o, H_o, R, e \rangle &\rightarrow v' \\ S''_o = S_o[x \rightarrow v'] \quad H'_o = H_o \quad v_o = \text{nr} \\ R' &= (R \cup C_b) - R_b \end{aligned}$$

First, we prove that $(S''_o, H'_o, v_o) \sim (S'_i, H'_i, v_i)$ using the hypothesis $(S_o, H_o) \sim (S_i, H_i)$. The equivalence is trivial for return values, because $v_i = \text{nr} = v_i$, and for heaps, because $H'_o = H_o$, $H'_i = H_i$, and $H_o \downarrow = H_i$. For stacks we have:

$$\begin{aligned} (S''_o(y)) \downarrow &= (S_o(y)) \downarrow = S_i(y) = S'_i(y) \text{ for } y \neq x \\ (S''_o(x)) \downarrow &= v' \downarrow = v = S'_i(x) \end{aligned}$$

because $v' \downarrow = v$ from the Expression Preservation Lemma. Hence $S''_o \downarrow = S'_i$. Therefore $(S''_o, H'_o, v_o) \sim (S'_i, H'_i, v_i)$.

Because assignments never return values, we only need to prove the conclusion of implication (57): $(S''_o, H'_o, R') \sim (\Gamma(c_i \bullet), T)$. We prove this relation using hypothesis relation (55): $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$.

We first prove that consistency relation (41) holds. By hypothesis, $(S_o, H_o) \sim (\Gamma(\bullet c_i), T)$. That is, there is a mapping μ which witnesses this consistency and satisfies relations (38)-(40). We show that the same mapping witnesses the equivalence $(S''_o, H_o) \sim (\Gamma(c_i \bullet), T)$. Since the heap and the mapping μ are unchanged, relations (38) and (40) trivially hold. Then, since the stack S''_o and the type environment $\Gamma(c_i \bullet)$ are the same as S_o and $\Gamma(\bullet c_i)$, except for x , it means that relation (39) holds for all $y \neq x$. Finally, consider (l, r) such that $S''_o(x) = (l, r)$. The analysis result is such that $x : \tau \in \Gamma(c_i \bullet)$, where $\Gamma(\bullet c_i) \vdash e : \tau$. By the operational semantics of assignments, the execution of c_o must evaluate the assigned expression: $\langle S_o, H_o, R, e \rangle \rightarrow (l, r)$. By hypothesis, $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$. We can apply Property 4 and get: $\mu(l) = (\tau, r)$. Hence, we proved that:

$$S''_o(x) = (l, r) \Rightarrow x : \tau \in \Gamma(c_i \bullet) \wedge \mu(l) = (\tau, r)$$

Thus, relation (39) is satisfied for all x . This proves that $(S''_o, H_o) \sim (\Gamma(c_i \bullet), T)$.

Using Property 1 and the fact that R_b and $\text{Live}(c_i \bullet)$ are disjoint by definition of R_b , we conclude that $\neg \text{reach}(S''_o, H'_o, r)$ for each $r \in R_b$. Therefore, by the operational semantics of the region removal command we have that $S'_o = S''_o$.

Finally, we prove condition (42), that R' is consistent with the set of live regions after the statement. We have:

$$\begin{aligned} C_b &= \text{Live}(c_i \bullet) - \text{Live}(\bullet c_i) \\ R_b &= \text{Live}(\bullet c_i) - \text{Live}(c_i \bullet) \\ R &= \text{Live}(\bullet c_i) \end{aligned}$$

Hence:

$$R' = (R \cup C_b) - R_b = \text{Live}(c_i \bullet)$$

This relation is due to the set equality:

$$(A \cup (B - A)) - (A - B) = B$$

with $A = \text{Live}(\bullet c_i)$ and $B = \text{Live}(c_i \bullet)$.

This proves that $(S'_o, H'_o, R') \sim (\Gamma(c_i \bullet), T)$ and completes the proof for this case.

Rule for c_i ($x.f = e$). The translation of c_i is:

$$c_o \equiv (\text{create } C_b; c_i; \text{remove } R_b)$$

Consider input and output states satisfying conditions (52)-(55). From the operational semantics of the input and output languages, we have:

$$\begin{aligned} S'_i = S_i \quad & S_i(x) = l_0 \quad \langle S_i, H_i, e \rangle \rightarrow v \\ & H'_i = H_i[l_0 \mapsto H_i(l_0)[f \mapsto v]] \quad v_i = \text{nr} \\ \\ S''_o = S_o \quad & S_o(x) = (l'_0, r_0) \quad \langle S_o, H_o, R, e \rangle \rightarrow v' \\ & H'_o = H_o[l'_0 \mapsto H_i(l'_0)[f \mapsto v']] \quad v_o = \text{nr} \\ & r_0 \in R \cup C_b \quad R' = (R \cup C_b) - R_b \end{aligned}$$

First, we prove that $(S''_o, H'_o, v_o) \sim (S'_i, H'_i, v_i)$ using the hypothesis $(S_o, H_o) \sim (S_i, H_i)$. The equivalence is trivial for return values, because $v_i = \text{nr} = v_o$, and for stacks, because $S''_o = S_o$, $S'_i = S_i$, and $S_o \downarrow = S_i$. From the stack consistency $S_o \downarrow = S_i$ we have: $(l'_0, r) \downarrow = l_0$, so $l'_0 = l_0$. For heaps we have:

$$\begin{aligned} (H'_o(l'')) \downarrow &= (H_o(l'')) \downarrow = H_i(l'') = H'_i(l'') \text{ for } l'' \neq l_0 \\ (H'_o(l_0)) \downarrow &= (H_o(l_0)[f \mapsto v']) \downarrow = H_i(l_0)[f \mapsto v] = H'_i(l_0) \end{aligned}$$

because $v' \downarrow = v$ from the Expression Preservation Lemma. Hence $H'_o \downarrow = H'_i$. Therefore $(S''_o, H'_o, v_o) \sim (S'_i, H'_i, v_i)$.

Because assignments never return values, we only need to prove the conclusion of implication (57): $(S''_o, H'_o, R') \sim (\Gamma(c_i \bullet), T)$. We prove this relation using hypothesis relation (55): $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$.

We first prove that consistency relation (41) holds. By hypothesis, $(S_o, H_o) \sim (\Gamma(\bullet c_i), T)$. That is, there is a mapping μ which witnesses this consistency and satisfies relations (38)-(40). We show that the same mapping witnesses the equivalence $(S''_o, H_o) \sim (\Gamma(c_i \bullet), T)$. Since the stack and the type environment are unchanged after the execution of c_i , and we use the same mapping μ , relations (38) and (39) trivially hold. Then, since the stack H'_o is the same as H_o , except for l_0 and field f , it means that relation (40) holds for all $l' \neq l_0$.

Finally, consider $l' = l_0$ and (l_1, r_1) such that $H'_o(l_0)(f) = (l_1, r_1)$. Since $S_o(x) = (l_0, r_0)$, by hypothesis $((S_o, H_o) \sim (\Gamma(\bullet c_i), T))$ and relation (39), we have $\mu(l_0) = (\tau_0, r_0)$. The analysis result is such that $\Gamma(\bullet c_i) \vdash x.f : \tau$. By the typing rules, we get $x : \text{ref}[\rho_0, \bar{\rho}](\bar{\tau} \bar{f}) \in \Gamma(\bullet c_i)$ and $\tau \bar{f} \in \bar{\tau} \bar{f}$. By relation (39), we have $r_0 = \text{ref}[\rho_0, \bar{\rho}](\bar{\tau} \bar{f})$, so:

$$\mu(l_0) = (\text{ref}[\rho_0 \bar{\rho}](\bar{\tau} \bar{f}), r_0) \quad \tau \bar{f} \in \bar{\tau} \bar{f}$$

By the operational semantics of assignments, the execution of c_o must evaluate the expression. Because $H'_o(l_0)(f) = (l_1, r_1)$, the evaluation must yield a location: $\langle S_o, H_o, R, e \rangle \rightarrow (l_1, r_1)$. We

also know that the analysis result is such that $\Gamma(\bullet c_i) \vdash e : \tau'$, with $\tau' <: \tau$. Suppose $\tau' \neq \tau$. Then, by the definition of subtyping, $\tau' = \perp$. By property 5, $\langle S_o, H_o, R, e \rangle \rightarrow \text{null}$, contradiction. Hence $\tau' = \tau$ and $\Gamma(\bullet c_i) \vdash e : \tau'$. Also, by hypothesis, $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$. We can apply Property 4 and get: $\mu(l_1) = (\tau, r_1)$. Therefore, we proved that:

$$\begin{aligned} H(l_0)(f) = (l_1, r_1) &\Rightarrow \mu(l_0) = (\text{ref}[\rho_0, \bar{\rho}] (\bar{\tau} \bar{f}), r') \wedge \\ &\mu(l_1) = (\tau, r_1) \wedge \tau \bar{f} \in \bar{\tau} \bar{f} \end{aligned}$$

Thus, relation (40) is satisfied for all x , so we have that $(S''_o, H_o) \sim (\Gamma(c_i \bullet), T)$.

Using Property 1 and the fact that R_b and $\text{Live}(c_i \bullet)$ are disjoint by definition of R_b , we conclude that $\neg \text{reach}(S''_o, H'_o, r)$ for each $r \in R_b$. Therefore, by the operational semantics of the region removal command we have that $S'_o = S''_o$.

Finally, we can show the consistency relation (42), $R' = (R \cup C_b) - R_b = \text{Live}(c_i \bullet)$, using the same proof as in the case for $x = e$. Hence, $(S'_o, H'_o, R') \sim (\Gamma(c_i \bullet), T)$. This completes the proof for this case.

Rule for $c_i = (x = \text{new } s)$. The translation of c_i is :

$$c_o \equiv (\text{create } C_b; x = \text{new } s \text{ in } r; \text{remove } R_b)$$

where:

$$\Delta \vdash a : \text{ref } [\rho_0, \bar{\rho}] (\bar{\tau} \bar{f}) \quad r = T[\rho_0]$$

Consider input and output states satisfying conditions (52)-(55). Consider that l is the newly created location. We assume that the same location l is created during the evaluation of the input and output languages, whenever the evaluation starts in consistent input and output states. Therefore $l \notin \text{dom}(H_i)$ and $l \notin \text{dom}(H_o)$. From the operational semantics of the input and output languages, we have:

$$\begin{aligned} S'_i &= S_i[x \mapsto l] & H'_i &= H_i \cup \{l \mapsto m\} & v_i &= nr \\ S''_o &= S_o[x \mapsto (l, r)] & H'_o &= H_o \cup \{l \mapsto m\} & v_o &= nr \\ R' &= (R \cup C_b) - R_b & m &= \text{init}(\bar{t}) & s &: \text{ref}(\bar{t} \bar{f}) \end{aligned}$$

From the relations, we immediately have $(S''_o, H'_o, v_o) \sim (S'_i, H'_i, v_i)$ because $v_o = v_i = nr$, $(S_o, H_o) \sim (S_i, H_i)$ and:

$$\begin{aligned} (S''_o(x)) \downarrow &= (l, r) \downarrow = l = S'_i(x) \\ (H'_o(l)) \downarrow &= m \downarrow = m = H'_i(l) \end{aligned}$$

Because assignments don't return values, we only need to prove the conclusion of implication (57): $(S'_o, H'_o, R') \sim (\Gamma(c_i \bullet), T)$.

We first prove that consistency relation (41) holds. By hypothesis, $(S_o, H_o) \sim (\Gamma(\bullet c_i), T)$. That is, there is a mapping μ which witnesses this consistency and satisfies relations (38)-(40). We show that extended mapping $\mu' = \mu \cup \{l \mapsto (\tau, r)\}$ is a witness for the consistency $(S'_o, H'_o) \sim (\Gamma(c_i \bullet), T)$, where type τ is the type of the allocation command: $\tau = \text{ref } [\rho_0, \bar{\rho}] (\bar{\tau} \bar{f})$ and $r = T[\rho_0]$.

From hypothesis, $(S_o, H_o) \sim (\Gamma(\bullet c_i), T)$. Since S''_o updates S_o with a new value for x , $\Gamma(c_i \bullet)$ updates $\Gamma(\bullet c_i)$ with a new type for x , and H'_o extends H_o with a value for l , it means that: relation (38) holds for all locations except l , relation (39) holds for all variables except x , and relation (40) holds for all locations except l . We next prove that these relations also hold for l and x .

Relation (38) immediately holds for the new location l , by construction of the extended mapping μ' : $\mu'(l) = (\tau, r)$, where $\tau = \text{ref } [\rho_0, \bar{\rho}] (\bar{\tau} \bar{f})$ and $r = T[\rho_0]$.

Relation (39) holds for variable x because all the relations in this equation hold: by the operational semantics we have $S_o''(x) = (l, r)$, by the analysis result relation (18) we get $\Gamma(c_i \bullet) \vdash x : \tau$, and by construction of μ' we have $\mu'(l) = (\tau, r)$.

Relation (40) trivially holds for the new location l , because the initialization function init sets all of the fields to 0 and null values. Hence $H_o'(l)(f) \in \{0, \text{null}\}$, so $H_o'(l)(f) \neq (l', r)$ for all fields f . Since the premise is false, the implication is trivially true.

Thus, μ' satisfies all of the relations (38) - (40) for all variables and all locations. This proves that $(S_o'', H_o') \sim (\Gamma(c_i \bullet), T)$.

Using Property 1 and the fact that R_b and $\text{Live}(c_i \bullet)$ are disjoint by definition of R_b , we conclude that $\neg \text{reach}(S_o'', H_o', r)$ for each $r \in R_b$. Therefore, by the operational semantics of the region removal command we have that $S_o' = S_o''$.

Finally, we can show the consistency relation (42), $R' = (R \cup C_b) - R_b = \text{Live}(c_i \bullet)$, using the same proof as in the case for $x = e$. Hence, $(S_o', H_o', R') \sim (\Gamma(c_i \bullet), T)$. This completes the proof for this case.

Rule for $c_i = (x = p(y))$. The translation of c_i is :

$$c_o \equiv (\text{create } C_p ; x = p[\bar{r}](y) ; \text{remove } R_b)$$

Consider input and output states satisfying conditions (52)-(55). Let c_{ip} be the command representing the body of the invoked procedure in the input program, and c_{op} the body of that procedure in the transformed program: $c_{op} = \mathcal{T}[c_{ip}]$. From the operational semantics of the input and output languages, we have:

$$\begin{aligned} S_i(y) = v_1 \quad & \langle \{z \mapsto v_1\}, H_i, c_{ip} \rangle \rightarrow \langle S_i'', H_i'', v_i'' \rangle \\ v_i'' \neq \text{nr} \quad & S_i' = S_i[x \mapsto v_i''] \quad H_i' = H_i'' \\ S_o(y) = v_1' \quad & \langle \{z \mapsto v_1'\}, H_o, \bar{r}, c_{op}[\bar{r}/\bar{r}_p] \rangle \rightarrow \langle S_o'', H_o'', \bar{r}, v_o'' \rangle \\ v_o'' \neq \text{nr} \quad & S_o''' = S_o[x \mapsto v_o''] \quad H_o' = H_o'' \\ \bar{r} \subseteq R'' \quad & R'' = R \cup C_b \quad R' = (R \cup C_b) - R_b \end{aligned}$$

By hypothesis, $(S_o, H_o) \sim (S_i, H_i)$. Hence, $v_1' \downarrow = v_1$. Thus, $(\{z \mapsto v_1'\}, H_o) \sim (\{z \mapsto v_1\}, H_i)$.

We can also show that $(\{z \mapsto v_1'\}, H_o, \bar{r}) \sim (\Gamma(\bullet c_{ip}), T)$, as follows. First, we obtain relation $(\{z \mapsto v_1'\}, H_o) \sim (\Gamma(\bullet c_{ip}), T)$ from the hypothesis $(S_o, H_o) \sim (\Gamma(\bullet c_i), T)$, since it can be witnessed using the same mapping μ . We only need to ensure condition (39) for variable z . This fact comes from the subtype relation $\Gamma(\bullet c_i)(y) <: \Gamma(\bullet c_{ip})(z)$. Second, we have that $\text{Live}(\bullet c_{ip}) = \bar{r}$, because the analysis starts the evaluation of the procedure body using an environment which has the argument type and the return type of the procedure.

We can apply the induction hypothesis for the translation of the procedure body and get:

$$\begin{aligned} (S_o'', H_o'', v_o'') & \sim (S_i'', H_i'', v_i'') \\ (\{\text{ret} \mapsto v_o''\}, H_o'') & \sim (\Gamma(\bullet c_{ip}), T) \end{aligned}$$

From the first relation, it follows that $(S_o[x \mapsto v_o''], H_o'') \sim (S_i[x \mapsto v_i''], H_i'')$. That is, $(S_o''', H_o') \sim (S_i', H_i')$.

From the second relation, we get $(S_o[x \mapsto v_o''], H_o'') \sim (\Gamma(c_i \bullet), T)$, using the same witness mapping μ . Therefore, $(S_o''', H_o') \sim (\Gamma(c_i \bullet), T)$. Using Property 1 and the fact that R_b and $\text{Live}(c_i \bullet)$ are disjoint by definition of R_b , we conclude that $\neg \text{reach}(S_o''', H_o', r)$ for each $r \in R_b$. Therefore, by the operational semantics of the region removal command we have that $S_o' = S_o'''$. Finally, we show the consistency relation (42), $R' = (R \cup C_b) - R_b = \text{Live}(c_i \bullet)$, using the same proof as in the case for $x = e$. Hence, $(S_o', H_o', R') \sim (\Gamma(c_i \bullet), T)$. This completes the proof for this case.

Rule for $c_i = \text{return } x$. The translation of c_i is:

$$c_o = \text{remove } R_r ; c_i$$

Consider input and output states satisfying conditions (52)-(54). From the operational semantics of the languages:

$$\begin{aligned} S_i(x) = v \quad S'_i = \emptyset \quad H'_i = H_i \quad v_i = v \\ S_o(x) = v'' \quad S'_o = \emptyset \quad H'_o = H_o \quad S''_o(x) = v_o = v' \\ R' = R \end{aligned}$$

Where S''_o is the stack after $\text{remove } R_r$ but before c_i . Since x is returned, by (??) we have that x doesn't reach any region in R_r . Thus, $S_o(x) = S''_o(x)$. Because $S_o \downarrow = S_i$, it means that $v' \downarrow = v$. Hence, we get $(S'_o, H'_o, v_o) \sim (S'_i, H'_i, v_i)$. Finally, we can show that $(\{\text{ret} \mapsto v_o\}, H'_o) \sim (\Gamma(\bullet c_i), T)$ using the same mapping μ as the one that witnesses the consistency relation $(S_o, H_o) \sim (\Gamma(\bullet c_i), T)$. As the heap is unchanged ($H'_o = H_o$), relations (38) and (40) trivially hold. For the stack, relation (39), we only need to prove that:

$$v_o = (l, r) \Rightarrow \text{ret} : \tau \in \Gamma(\bullet c_i) \wedge \mu(l) = (\tau, r)$$

Assume that $v_o = (l, r)$. The analysis result is such that $x : \tau' \in \Gamma(\bullet c_i)$ and $\tau' <: \tau'$. Assume that $\tau' \neq \tau$. Then $\tau' = \perp$. By hypothesis, $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$. By language semantics, $\langle S_o, H_o, R, x \rangle \rightarrow v_o$. By Property 5, $v_o = \text{null}$, contradiction. Therefore, $\tau' = \tau$ and $x : \tau \in \Gamma(\bullet c_i)$ and $\text{ret} : \tau \in \Gamma(\bullet c_i)$.

By hypothesis, $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$. Since $S_o(x) = v_o = (l, r)$ and $x : \tau \in \Gamma(\bullet c_i)$, it means that $\mu(l) = (\tau, r)$. Hence, we proved that $\text{ret} : \tau \in \Gamma(\bullet c_i)$ and $\mu(l) = (\tau, r)$. This completes the proof for this case and the proof of the theorem. \square

B.9 Progress Theorem

Theorem 2 (Progress) *Given a command c_i in the input language, a type environment $\Gamma(\bullet c_i)$ at the program point before the command, and a translation T , we have that:*

$$c_o = T[[c_i]] \tag{68}$$

$$\langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle \tag{69}$$

$$(S_o, H_o) \sim (S_i, H_i) \tag{70}$$

$$(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T) \tag{71}$$

\Rightarrow

$$\langle S_o, H_o, R, c_o \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle \tag{72}$$

PROOF. The proof is by induction on the derivations of the evaluation $\langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$.

Rule for $c_i = s \ x$. The translation of c_i is

$$\begin{aligned} c_o &= (s \ x ; c_{i1}) ; \text{remove } R_d \\ R_d &= \text{Live}(c_{i1} \bullet) - \text{Live}(c_i \bullet) \end{aligned}$$

Consider input and output states satisfying conditions (69)-(71). From semantic rule for this case:

$$\langle S_i \cup \{x \mapsto \text{null}\}, H_i, c_{i1} \rangle \rightarrow \langle S'_i \cup \{x \mapsto v_{xi}\}, H'_i, v_i \rangle$$

By induction hypothesis for the translation $c_{o1} = \mathcal{T}[\llbracket c_{i1} \rrbracket]$, command c_{o1} evaluates to a value:

$$\langle S_o \cup \{x \mapsto \text{null}\}, H_o, R, c_{o1} \rangle \rightarrow \langle S'', H'_o, R'', v_o \rangle$$

From the Preservation Theorem, the resulting output state is consistent with $\langle S'_i \cup \{x \mapsto v_{xi}\}, H'_i, v_i \rangle$. Therefore, S'' is of the form $S'' = S_o \cup \{x \mapsto v_{xo}\}$. Hence, we can apply the evaluation rule for declarations:

$$\langle S_o, H_o, R, c_o \rangle \rightarrow \langle S'_o, H'_o, R'', v_o \rangle$$

If $v_o \neq \text{nr}$, this completes the evaluation of c_o . Otherwise, $R'' = \text{Live}(c_{i1} \bullet)$, so $R_d \subseteq R''$. Thus, the evaluation can proceed and execute the trailing region removal commands.

Rule for $c_i = \text{int } x$. The proof is similar to the case above.

Rule 1 for $c_i = c_{i1} ; c_{i2}$. Let $c_{o1} = \mathcal{T}[\llbracket c_{i1} \rrbracket]$ and $c_{o2} = \mathcal{T}[\llbracket c_{i2} \rrbracket]$. The translation of c_i is $c_o = c_{o1} ; c_{o2}$. Consider input and output states satisfying conditions (69)-(71). From the semantic rule considered in this case, we have:

$$\langle S_i, H_i, c_{i1} \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle, \quad v_i \neq \text{nr}$$

By induction hypothesis for the translation c_{i1} with the above evaluation, we have:

$$\langle S_o, H_o, R, c_{o1} \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle$$

By Preservation, $v_o \neq \text{nr}$, so:

$$\langle S_o, H_o, R, c_o \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle$$

which proves that the evaluation of c_o doesn't get stuck.

Rule 2 for $c_i = c_{i1} ; c_{i2}$. Let $c_{o1} = \mathcal{T}[\llbracket c_{i1} \rrbracket]$ and $c_{o2} = \mathcal{T}[\llbracket c_{i2} \rrbracket]$. The translation of c_i is $c_o = c_{o1} ; c_{o2}$. Consider input and output states satisfying conditions (69)-(71). From the semantic rule considered in this case, we have:

$$\begin{aligned} \langle S_i, H_i, c_{i1} \rangle &\rightarrow \langle S''_i, H''_i, \text{nr} \rangle \\ \langle S''_i, H''_i, c_{i2} \rangle &\rightarrow \langle S'_i, H'_i, v_i \rangle \end{aligned}$$

By induction hypothesis for the translation of c_{i1} with the evaluation $\langle S_i, H_i, c_{i1} \rangle \rightarrow \langle S''_i, H''_i, \text{nr} \rangle$, command c_{o1} evaluates to a value, and by the Preservation Theorem, that value is nr :

$$\langle S_o, H_o, R, c_{o1} \rangle \rightarrow \langle S''_o, H''_o, R'', \text{nr} \rangle$$

Also, by the Preservation Theorem, $(S''_o, H''_o) \sim (S'_i, H'_i)$ and $(S''_o, H''_o, R'') \sim (\Gamma(c_{i1} \bullet), \mathcal{T})$. Because $\Gamma(c_{i1} \bullet) = \Gamma(\bullet c_{i2})$, we can apply the induction hypothesis for the translation of c_{i2} with the evaluation $\langle S''_i, H''_i, c_{i2} \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$, and get:

$$\langle S''_o, H''_o, R'', c_{o2} \rangle \rightarrow \langle S'_o, H'_o, R', v_o \rangle$$

From the evaluations of c_{o1} and c_{o2} , we conclude that the evaluation of c_o doesn't get stuck.

Rule 1 for $c_i = \text{if } (e) \text{ then } c_{i1} \text{ else } c_{i2}$. This rule evaluates the true branch c_{i1} of the command. Let $c_{o1} = \mathcal{T}[\llbracket c_{i1} \rrbracket]$ and $c_{o2} = \mathcal{T}[\llbracket c_{i2} \rrbracket]$. The translation of c_i is:

$$c_o = \text{if } (e) \text{ then } (c_{o1} ; \text{create } C_{c1}) \text{ else } (c_{o2} ; \text{create } C_{c2})$$

Consider input and output states satisfying conditions (69)-(71). From the semantic rule considered in this case, we have:

$$\begin{aligned}\langle S_i, H_i, e \rangle &\rightarrow v \notin \{0, \text{null}\} \\ \langle S_i, H_i, c_{i1} \rangle &\rightarrow \langle S'_i, H'_i, v_i \rangle\end{aligned}$$

By Progress and Preservation of Expressions, e evaluates to a value $v' \notin \{0, \text{null}\}$:

$$\langle S_o, H_o, R, e \rangle \rightarrow v' \notin \{0, \text{null}\}$$

By induction hypothesis for the translation of c_{i1} with the evaluation $\langle S_i, H_i, c_{i1} \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$, command c_{o1} evaluates to a value v_o :

$$\langle S_o, H_o, R, c_{o1} \rangle \rightarrow \langle S'_o, H'_o, R'', v_o \rangle$$

If $v_o \neq \text{nr}$, this completes the evaluation of c_o . Otherwise, we use the Preservation Theorem, to get $(S'_o, H'_o, R'') \sim (\Gamma(c_{i1}\bullet), T)$. Hence, $R'' = \text{Live}(c_{i1}\bullet)$, so $R'' \cap C_{c1} = \emptyset$. Therefore, the evaluation is not stuck and can execute the trailing region creation commands.

Rule 2 for $c_i = \text{if } (e) \text{ then } c_{i1} \text{ else } c_{i2}$. This rule evaluates the false branch c_{i2} of the command and the proof is similar to the previous case.

Rules for $c_i = \text{while } (e) \text{ } c_{i1}$. We consider all of the three rules for the evaluation of while loops. The conclusion of all of these rules is:

$$(S_i, H_i, \text{while } (e) \text{ } c_{i1}) \rightarrow (S'_i, H'_i, v_i)$$

Let $c_{o1} = \mathcal{T}[\![c_{i1}]\!]$. The translation of c_i is:

$$c_o = \text{create } C_{w1}; \text{while } (e) \text{ } (c_{o1}; \text{create } C_{w2})$$

Consider input and output states satisfying conditions (69)-(71). The output command first executes the region creation commands $\text{create } C_{w1}$, and then evaluates the loop. Since $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$, $R = \text{Live}(\bullet c_i)$, so $R \cap C_{w1} = \emptyset$. Therefore, we can evaluate the sequence of region commands $\text{create } C_{w1}$:

$$\begin{aligned}\langle S_o, H_o, R, \text{create } C_{w1} \rangle &\rightarrow \langle S_o, H_o, R_{w1}, \text{nr} \rangle \\ \text{where: } R_{w1} &= R \cup C_{w1}\end{aligned}$$

By induction hypothesis, each sub-derivation of the evaluation $\langle S_i, H_i, c_i \rangle \rightarrow \langle S'_i, H'_i, v_i \rangle$ satisfies the implication in the theorem. These sub-derivations include the evaluation of c_{i1} in the second and the third rule for loops. Given this fact, the following property holds:

Property 7 Consider the commands c_{i1} and c'_{o1} in the current case. Let $(S_{pi}, H_{pi}), (S'_{pi}, H'_{pi}, v_i)$ (S_{po}, H_{po}, R_p) be states and values such that:

$$\langle S_{pi}, H_{pi}, \text{while } (e) \text{ } c_{i1} \rangle \rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle \quad (73)$$

$$(S_{po}, H_{po}) \sim (S_{pi}, H_{pi}) \quad (74)$$

$$(S_{po}, H_{po}, R_p) \sim (\Gamma(\bullet c_{i1}), T) \quad (75)$$

and such that the derivation for:

$$(S_{pi}, H_{pi}, \text{while } (e) \text{ } c_{i1}) \rightarrow (S'_{pi}, H'_{pi}, v_{pi})$$

is a sub-derivation of, or is the same as, the derivation for:

$$(S_i, H_i, \text{while } (e) \text{ } c_{i1}) \rightarrow (S'_i, H'_i, v_i)$$

Then, there exists $(S'_{po}, H'_{po}, R'_p, v_o)$ such that:

$$\langle S_{po}, H_{po}, R_p, \text{while } (e) \text{ } c'_{o1} \rangle \rightarrow \langle S'_{po}, H'_{po}, R'_p, v_{po} \rangle \quad (76)$$

Using this result, we directly get the required relations for this case, using the following instantiation:

$$\begin{aligned} (S_{pi}, H_{pi}) &= (S_i, H_i) & (S'_{pi}, H'_{pi}) &= (S'_i, H'_i) & v_{pi} &= v_i \\ (S_{po}, H_{po}) &= (S_o, H_o) & (S'_{po}, H'_{po}) &= (S'_o, H'_o) & v_{po} &= v_o \\ R_p &= R_{w1} & R'_p &= R' \end{aligned}$$

We can apply the property because one can show that: $(S_o, H_o, R_{w1}) \sim (\Gamma(\bullet c_{i1}), T)$, using the same proof as the one from the corresponding case in the Preservation Theorem.

We next prove the property by induction on the derivations of $\langle S_{pi}, H_{pi}, \text{while } (e) \ c_{i1} \rangle \rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle$. Consider input and output states and values satisfying relations (73) - (75). From condition (73), one of the three rules for while loops in the input operational semantics must apply. We consider each of these rules in turn.

Subcase: While Rule 1. Assume the first rule for while loops applies:

$$\begin{aligned} \langle S_{pi}, H_{pi}, e \rangle &\rightarrow v \quad v \in \{0, \text{null}\} \\ S'_{pi} &= S_{pi} \quad H'_{pi} = H_{pi} \quad v_{pi} = \text{nr} \end{aligned}$$

By Progress and Preservation of Expressions, e evaluates to a value $v' \in \{0, \text{null}\}$ in the transformed program: $\langle S_o, H_o, e \rangle \rightarrow v' \in \{0, \text{null}\}$. Therefore, we can evaluate $\text{while } (e) \ c'_{o1}$ using the first rule for while loops in the output operational semantics.

Subcase: While Rule 2. Assume the second rule for while loops applies:

$$\begin{aligned} \langle S_{pi}, H_{pi}, e \rangle &\rightarrow v \quad v \notin \{0, \text{null}\} \\ \langle S_{pi}, H_{pi}, c_{i1} \rangle &\rightarrow \langle S_{pi}, H_{pi}, v_{pi} \rangle \\ S'_{pi} &= S_{pi} \quad H'_{pi} = H_{pi} \quad v_{pi} \neq \text{nr} \end{aligned}$$

By Progress and Preservation of Expressions, e evaluates to a value $v' \notin \{0, \text{null}\}$ in the transformed program: $\langle S_o, H_o, e \rangle \rightarrow v' \notin \{0, \text{null}\}$. We can apply the *outer induction hypothesis* to the translation of c_{i1} with the evaluation $\langle S_{pi}, H_{pi}, c_{i1} \rangle \rightarrow \langle S_{pi}, H_{pi}, v_{pi} \rangle$, because we know that its derivation is a sub-derivation of $(S_i, H_i, \text{while } (e) \ c_{i1}) \rightarrow (S'_i, H'_i, v_i)$. We get:

$$\langle S_{po}, H_{po}, R_p, c_{o1} \rangle \rightarrow \langle S''_{po}, H''_{po}, R''_p, v'_{po} \rangle$$

By the Preservation Theorem, $v'_{po} \neq \text{nr}$. Thus, we can evaluate $\text{while } (e) \ c'_{o1}$ using the second rule for while loops in the output operational semantics.

Subcase: While Rule 3. Assume the third rule for while loops applies:

$$\begin{aligned} \langle S_{pi}, H_{pi}, e \rangle &\rightarrow v \quad v \notin \{0, \text{null}\} \\ \langle S_{pi}, H_{pi}, c_{i1} \rangle &\rightarrow \langle S''_{pi}, H''_{pi}, \text{nr} \rangle \\ \langle S''_{pi}, H''_{pi}, c_i \rangle &\rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle \end{aligned}$$

By Progress and Preservation of Expressions, e evaluates to a value $v' \notin \{0, \text{null}\}$ in the transformed program: $\langle S_o, H_o, e \rangle \rightarrow v' \notin \{0, \text{null}\}$. We can apply the *outer induction hypothesis*, for the translation of c_{i1} with the evaluation $\langle S_{pi}, H_{pi}, c_{i1} \rangle \rightarrow \langle S''_{pi}, H''_{pi}, \text{nr} \rangle$, and get:

$$\langle S_{po}, H_{po}, R_p, c_{o1} \rangle \rightarrow \langle S''_{po}, H''_{po}, R''_p, v'_{po} \rangle$$

By Preservation, $v'_{po} = \text{nr}$, $(S''_{po}, H''_{po}) \sim (S''_{pi}, H''_{pi})$, and $(S''_{po}, H''_{po}, R''_p) \sim (\Gamma(c_{i1}\bullet), T)$. Hence, $R''_p = \text{Live}(c_{i1}\bullet)$, so $R''_p \cap C_{w2} = \emptyset$. Therefore, we can evaluate the sequence of region commands $\text{create } C_{w2}$:

$$\langle S''_{po}, H''_{po}, R''_p, \text{create } C_{w2} \rangle \rightarrow \langle S''_{po}, H''_{po}, R''_p \cup C_{w2}, \text{nr} \rangle$$

One can show that: $(S''_{po}, H''_{po}, R''_p \cup C_{w2}) \sim (\Gamma(c_{i1}\bullet), \mathcal{T})$, using the same proof as the one from the corresponding case in the Preservation Theorem. We can then apply the *inner induction hypothesis* for $\langle S''_{pi}, H''_{pi}, c_i \rangle \rightarrow \langle S'_{pi}, H'_{pi}, v_{pi} \rangle$ and get:

$$\langle S''_{po}, H''_{po}, R''_p \cup C_{w2}, \text{while } (e) \ c'_{o1} \rangle \rightarrow \langle S'_{po}, H'_{po}, R'_p, v_{po} \rangle$$

Thus, we can evaluate $\text{while } (e) \ c'_{o1}$ in state (S_{po}, H_{po}, R_p) using the third rule for while loops in the output operational semantics. This concludes the proof of the property.

Rule for $c_i = (x = e)$. The translation of c_i is:

$$c_o = \text{create } C_b; c_i; \text{remove } R_b$$

Consider input and output states satisfying conditions (69)-(71). By condition (71), $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), \mathcal{T})$, so $R = \text{Live}(\bullet c_i)$. Thus, $R \cap C_b = \emptyset$. Therefore, the execution can proceed by evaluating the region creation commands:

$$\langle S_o, H_o, R, \text{create } C_b \rangle \rightarrow \langle S_o, H_o, R \cup C_b, nr \rangle$$

From the semantic rule considered in this case:

$$\begin{aligned} \langle S_i, H_i, e \rangle &\rightarrow v \\ S'_i = S_i[x \rightarrow v] \quad H'_i = H_i \quad v_i = nr \end{aligned}$$

By condition (70), and Progress and Preservation for Expressions:

$$\langle S_o, H_o, e \rangle \rightarrow v'$$

Hence, we can further evaluate c_i in the transformed program:

$$\begin{aligned} \langle S_o, H_o, R \cup C_b, c_i \rangle &\rightarrow \langle S'_o, H_o, R \cup C_b, nr \rangle \\ S'_o = S_o[x \rightarrow v'] \end{aligned}$$

We know that $R_b \subseteq \text{Live}(\bullet c_i)$ and $R = \text{Live}(\bullet c_i)$. Thus, $R_b \subseteq R \cup C_b$, so the evaluation can proceed and execute the trailing region removal commands.

Rule for $c_i = (x.f = e)$. The translation of c_i is:

$$c_o = \text{create } C_b; c_i; \text{remove } R_b$$

Consider input and output states satisfying conditions (69)-(71). By condition (71), $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), \mathcal{T})$, so $R = \text{Live}(\bullet c_i)$. Thus, $R \cap C_b = \emptyset$. Therefore, the execution can proceed by evaluating the region creation commands:

$$\langle S_o, H_o, R, \text{create } C_b \rangle \rightarrow \langle S_o, H_o, R \cup C_b, nr \rangle$$

From the semantic rule considered in this case:

$$\begin{aligned} S_i(x) = l_0 \quad \langle S_i, H_i, e \rangle &\rightarrow v \\ S'_i = S_i \quad H'_i = H_i[l_0 \mapsto H_i(l_0)[f \mapsto v]] \quad v_i &= nr \end{aligned}$$

By condition (70), and Progress and Preservation for Expressions:

$$\langle S_o, H_o, e \rangle \rightarrow v'$$

Because $(S_o, H_o) \sim (S_i, H_i)$, $S_o(x) = (l_0, r_0)$. Thus, we have $\text{reach}(S_o, H_o, r_0)$. Also, by condition (71), $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$. By Property 2, $r_0 \in R$. So $r_0 \in R \cup C_b$.

Hence, we can further evaluate c_i in the transformed program:

$$\langle S_o, H_o, R \cup C_b, c_i \rangle \rightarrow \langle S_o, H'_o, R \cup C_b, nr \rangle$$

We know that $R_b \subseteq \text{Live}(\bullet c_i)$ and $R = \text{Live}(\bullet c_i)$. Thus, $R_b \subseteq R \cup C_b$, so the evaluation can proceed and execute the trailing region removal commands.

Rule for $c_i = (x = \text{new } s)$. The translation of c_i is :

$$c_o = \text{create } C_b ; x = \text{new } s \text{ in } r ; \text{remove } R_b$$

Consider input and output states satisfying conditions (69)-(71). By condition (71), $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$, so $R = \text{Live}(\bullet c_i)$. Thus, $R \cap C_b = \emptyset$. Therefore, the execution can proceed by evaluating the region creation commands:

$$\langle S_o, H_o, R, \text{create } C_b \rangle \rightarrow \langle S_o, H_o, R \cup C_b, nr \rangle$$

From the semantic rule considered in this case:

$$\begin{array}{ll} s : \text{ref}(\bar{t} \bar{f}) & m = \text{init}(\bar{t} \bar{f}) \\ S'_i = S_i[x \mapsto l] & H'_i = H_i \cup \{l \mapsto m\} \quad v_i = nr \end{array}$$

After the allocation statement, the analysis assigns to x the type of the allocation: $\Gamma(c_i \bullet)(x) = \text{ref} [\rho_0, \bar{\rho}] (\bar{t} \bar{f})$, with $\mathcal{T}[\rho_0] = r$. Therefore, $r \in \text{Live}(c_i \bullet)$. Hence, either $r \in \text{Live}(\bullet c_i) = R$, or $r \in \text{Live}(c_i \bullet) - \text{Live}(\bullet c_i) = C_b$. In either case, $r \in R \cup C_b$. Thus, the output program can further execute the allocation command:

$$\begin{array}{l} \langle S_o, H_o, R \cup C_b, c_i \rangle \rightarrow \langle S'_o, H'_o, R \cup C_b, nr \rangle \\ S'_o = S_o[x \mapsto l] \\ H'_o = H_o \cup \{l \mapsto m\} \end{array}$$

We know that $R_b \subseteq \text{Live}(\bullet c_i)$ and $R = \text{Live}(\bullet c_i)$. Thus, $R_b \subseteq R \cup C_b$, so the evaluation can proceed and execute the trailing region removal commands.

Rule for $c_i = (x = p(y))$. The translation of c_i is :

$$c_o \equiv (\text{create } C_p ; x = p [\bar{r}] (y) ; \text{remove } R_b)$$

Consider input and output states satisfying conditions (69)-(71). By condition (71), $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), T)$, so $R = \text{Live}(\bullet c_i)$. Thus, $R \cap C_b = \emptyset$. Therefore, the execution can proceed by evaluating the region creation commands:

$$\langle S_o, H_o, R, \text{create } C_b \rangle \rightarrow \langle S_o, H_o, R \cup C_b, nr \rangle$$

Let c_{ip} be the command representing the body of the invoked procedure in the input program, and c_{op} the body of that procedure in the transformed program: $c_{op} = \mathcal{T}[c_{ip}]$. From the semantic rule considered in this case:

$$\begin{array}{ll} S_i(y) = v_1 & \langle \{z \mapsto v_1\}, H_i, c_{ip} \rangle \rightarrow \langle S''_i, H''_i, v''_i \rangle \\ v''_i \neq nr & S'_i = S_i[x \mapsto v''_i] \quad H'_i = H''_i \end{array}$$

By hypothesis, $(S_o, H_o) \sim (S_i, H_i)$. Hence, $S_o(y) = v'_1$, with $v'_1 \downarrow = v_1$. Thus, $(\{z \mapsto v'_1\}, H_o) \sim (\{z \mapsto v_1\}, H_i)$. Also, by relation (??), $\bar{r} \subseteq R \cup C_b$.

We can also show that $(\{z \mapsto v'_1\}, H_o, \bar{r}) \sim (\Gamma(\bullet c_{ip}), \mathcal{T})$, in the same way we proved this result for the corresponding case in the Preservation Theorem. By the induction hypothesis for the translation of the procedure body c_{ip} with the evaluation $\langle \{z \mapsto v_1\}, H_i, c_{ip} \rangle \rightarrow \langle S''_i, H''_i, v''_i \rangle$, we get:

$$\langle \{z \mapsto v'_1\}, H_o, \bar{r}, c_{op}[\bar{r}/\bar{r}_p] \rangle \rightarrow \langle S''_o, H''_o, \bar{r}, v''_o \rangle$$

By the Preservation Theorem, $v''_o \downarrow = v''_i$. Hence, $v''_o \neq nr$.

All of the premises in the semantic rule for procedure calls in the output language are satisfied, so the evaluation can further execute the procedure call $x = p \ [\bar{r}] \ (y)$:

$$\begin{aligned} \langle S_o, H_o, R \cup C_b, x = p \ [\bar{r}] \ (y) \rangle &\rightarrow \langle S'_o, H'_o, R \cup C_b, nr \rangle \\ S'_o &= S_o[x \rightarrow v''_o] \\ H'_o &= H''_o \end{aligned}$$

We know that $R_b \subseteq \text{Live}(\bullet c_i)$ and $R = \text{Live}(\bullet c_i)$. Thus, $R_b \subseteq R \cup C_b$, so the evaluation can proceed and execute the trailing region removal commands.

Rule for $c_i = \text{return } x$. The translation of c_i is:

$$c_o = \text{remove } R_r ; c_i$$

Consider input and output states satisfying conditions (69)-(71). By condition (71), $(S_o, H_o, R) \sim (\Gamma(\bullet c_i), \mathcal{T})$, so $R = \text{Live}(\bullet c_i)$. Thus, $R_r \subseteq R$. Therefore, the execution can proceed by evaluating the region removal commands:

$$\langle S_o, H_o, R, \text{remove } R_r \rangle \rightarrow \langle S''_o, H_o, R - R_r, nr \rangle$$

Finally, from (69) and the operational semantics of the input language $S_i(x) = v$. But $(S_o, H_o) \sim (S_i, H_i)$, so $S_o(x) = v'$, with $v' \downarrow = v$. Since $\text{dom}(S_o) = \text{dom}(S''_o)$ we know that $S''_o(x) = v''$, for some v'' . We can therefore apply the semantic rule for return and execute the command. This completes the proof for this case and the proof of the theorem.

□